

FIG. 1

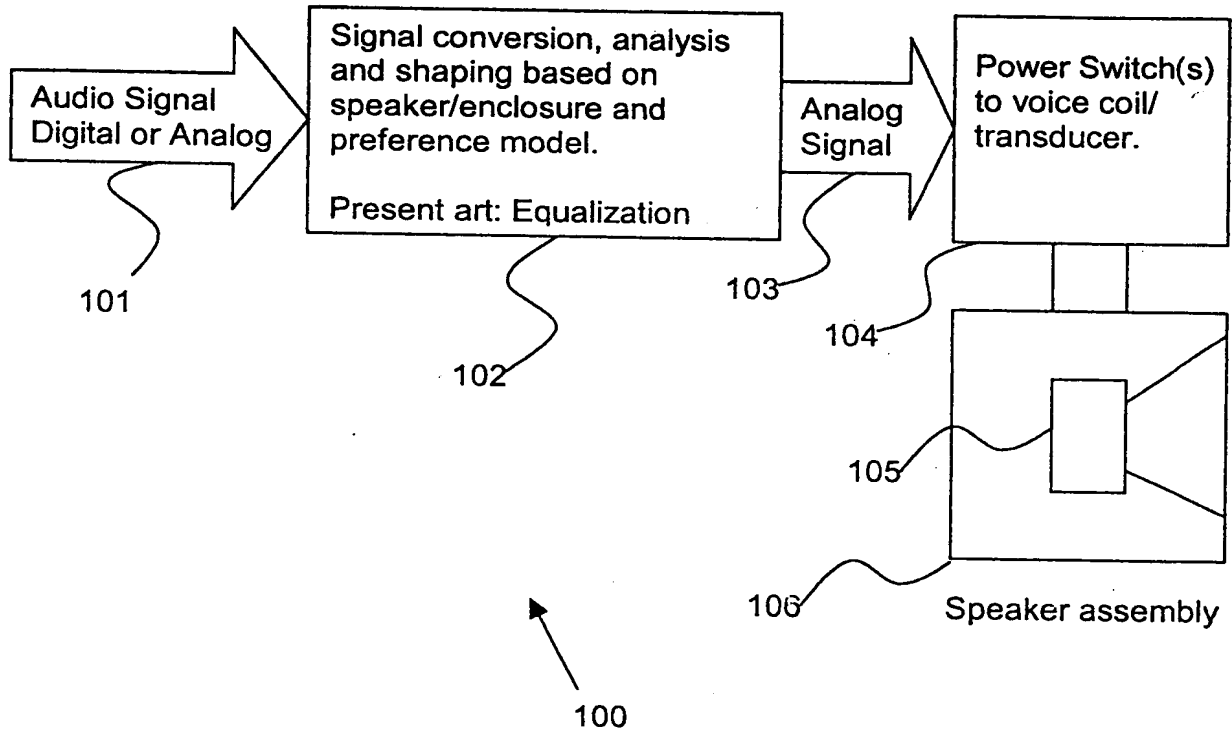


FIG. 2

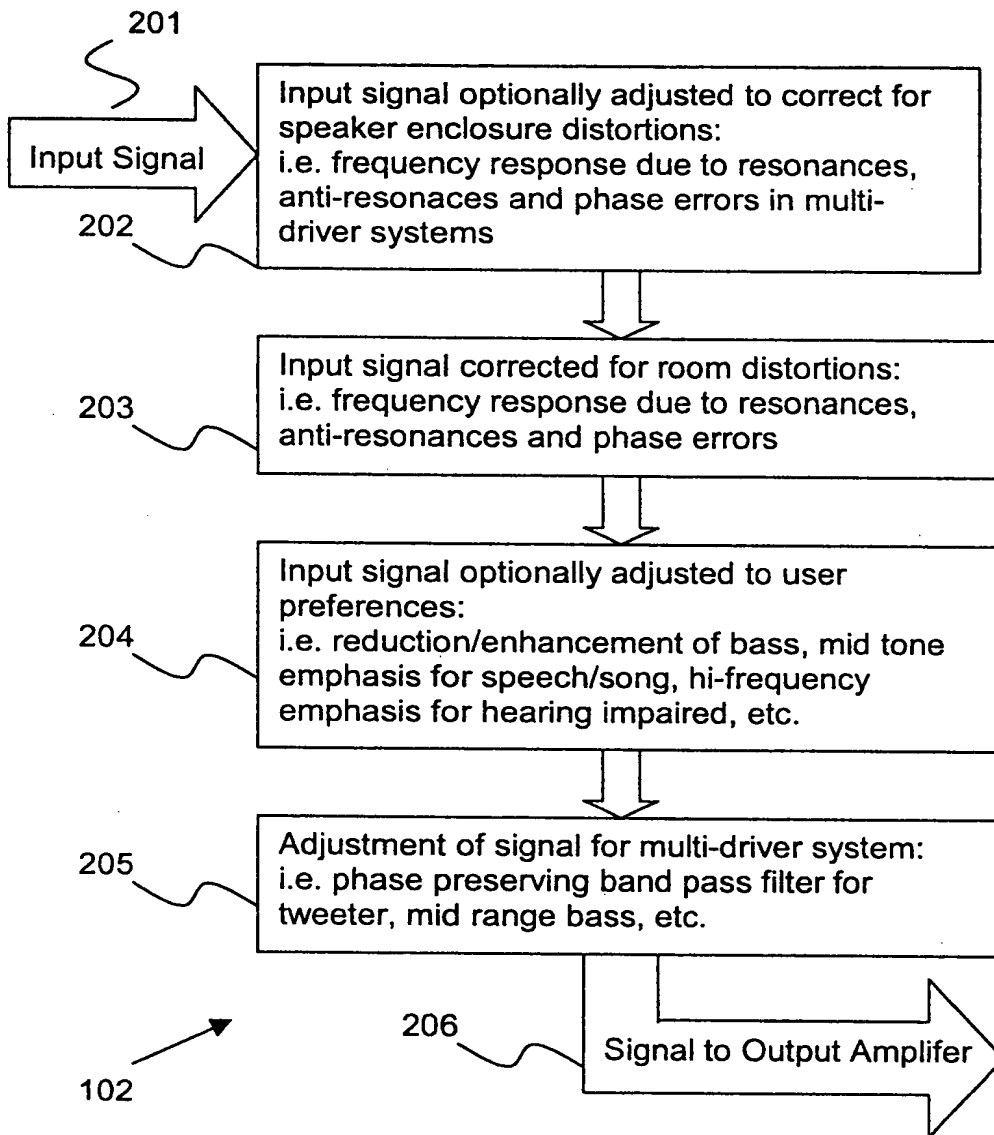


FIG. 3

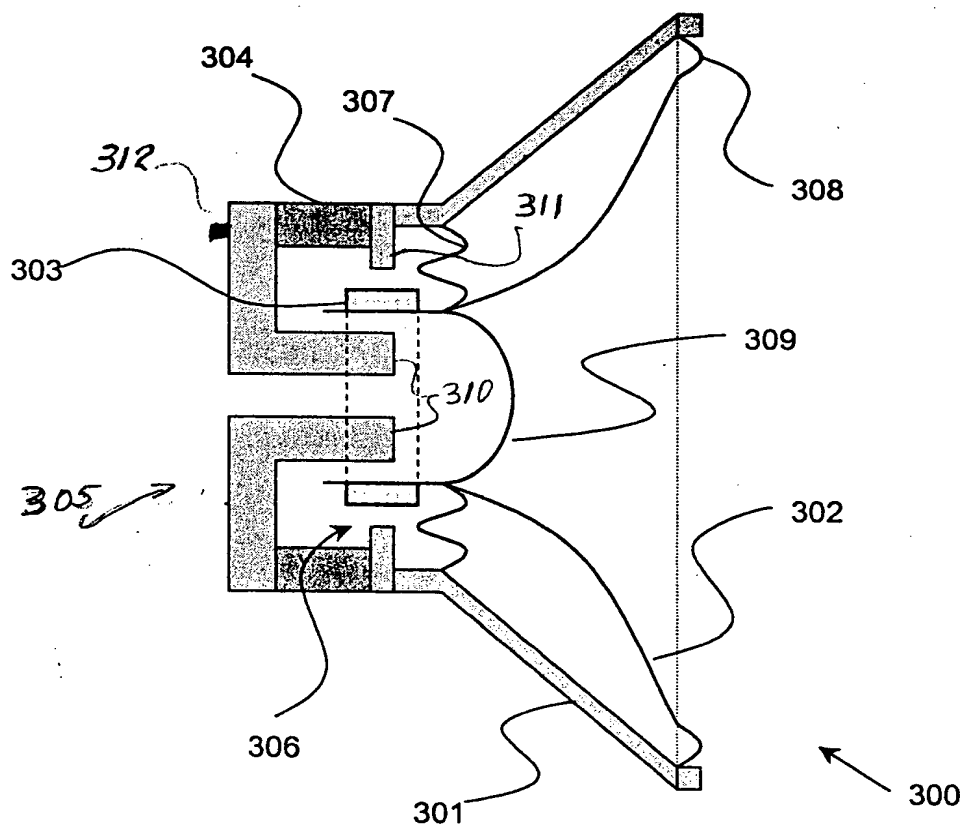


FIG. 4

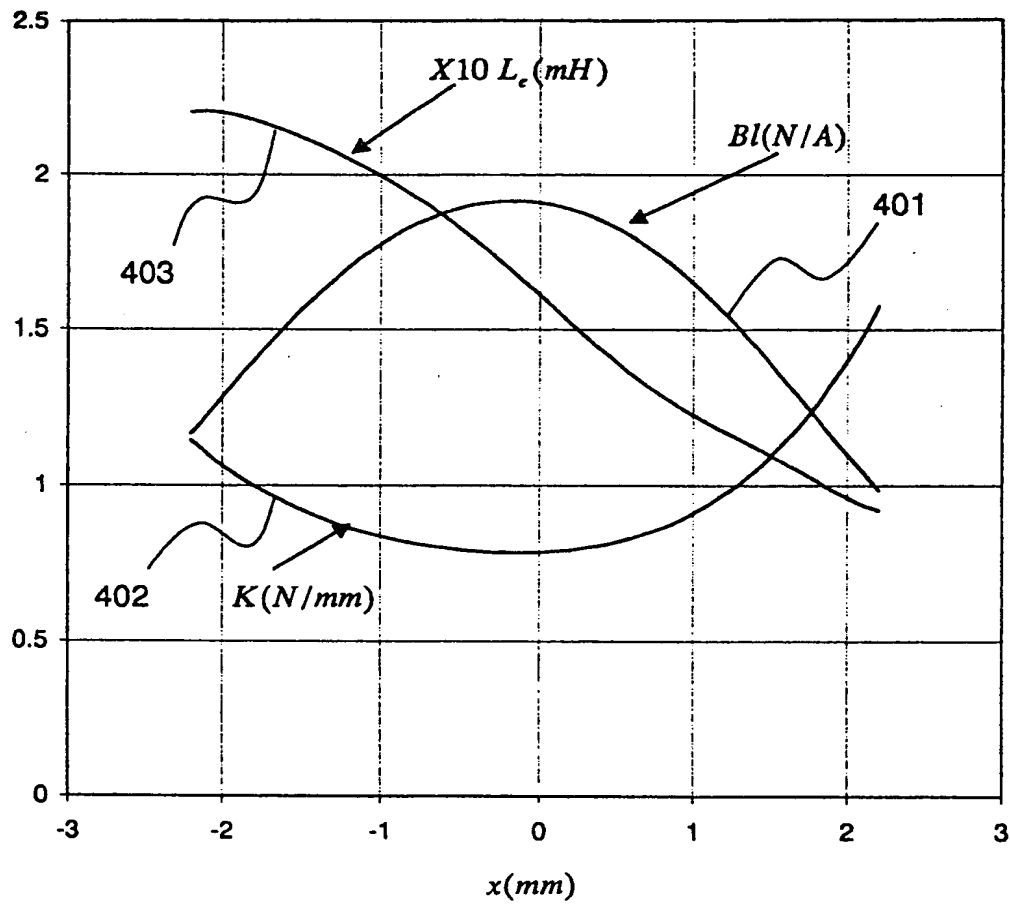


FIG. 5

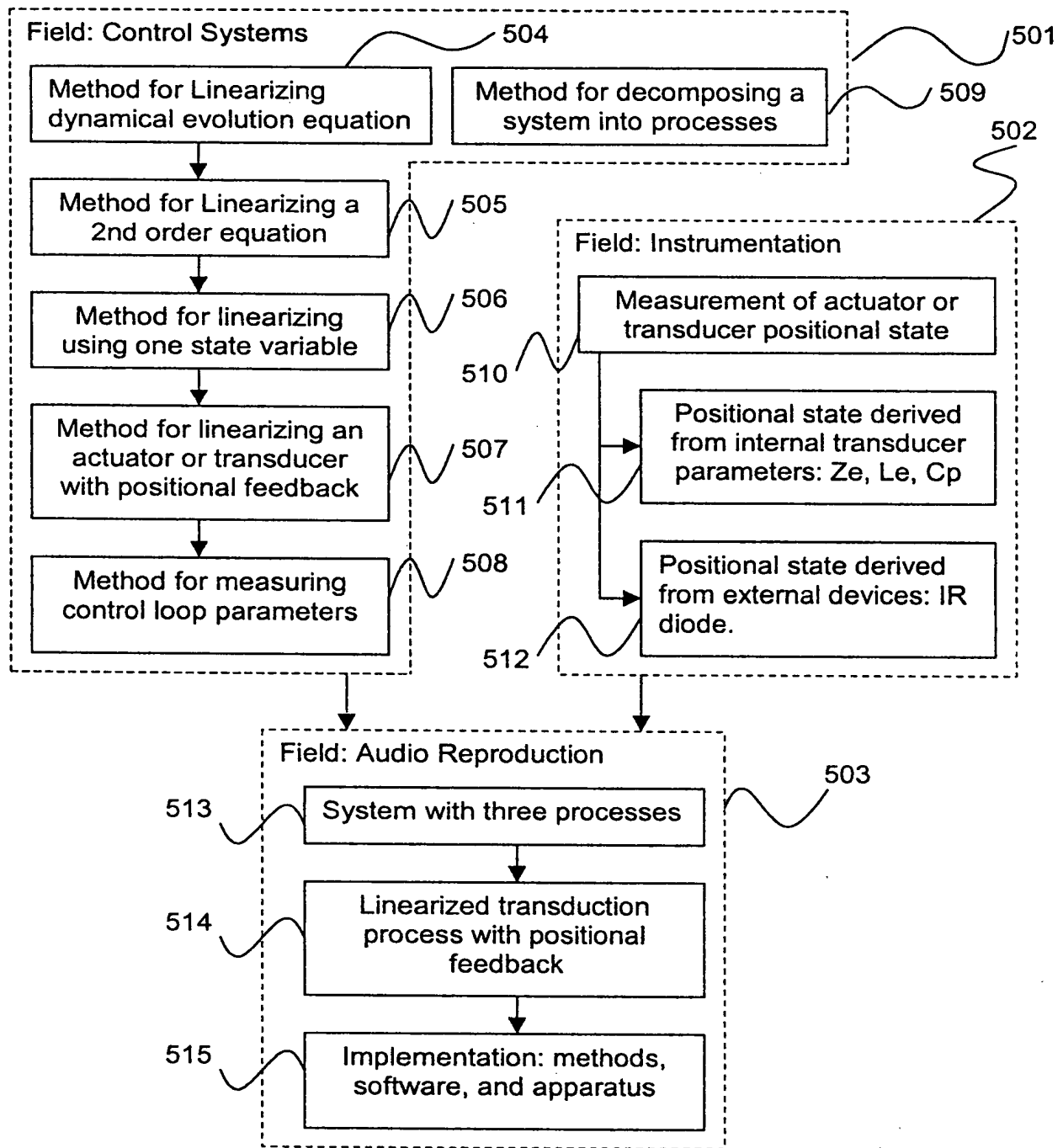


FIG. 6

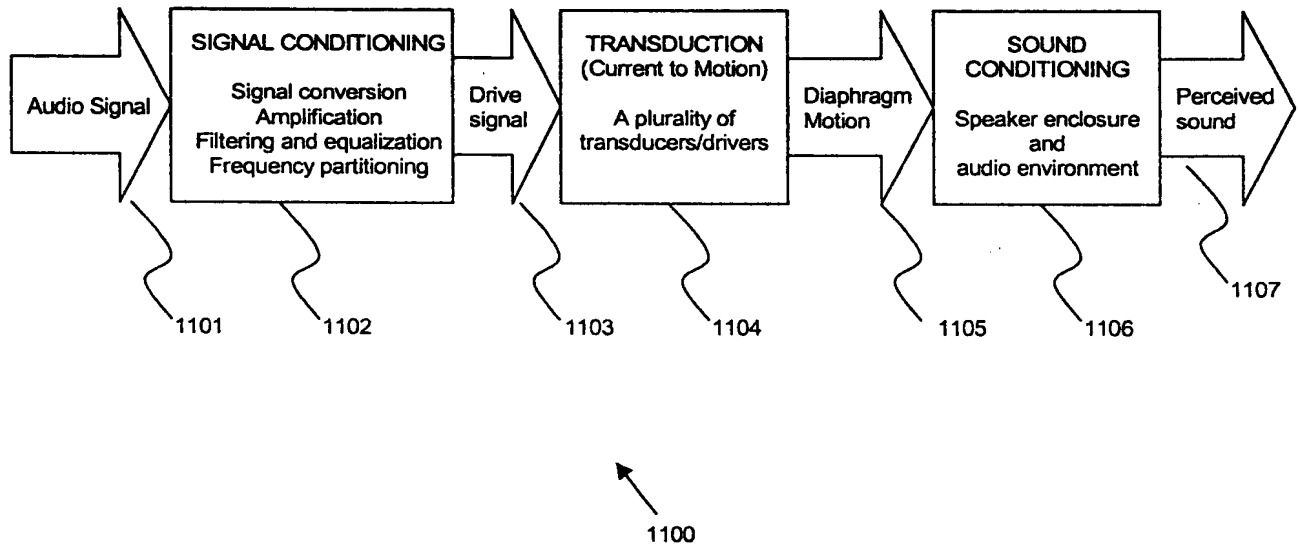


FIG. 7

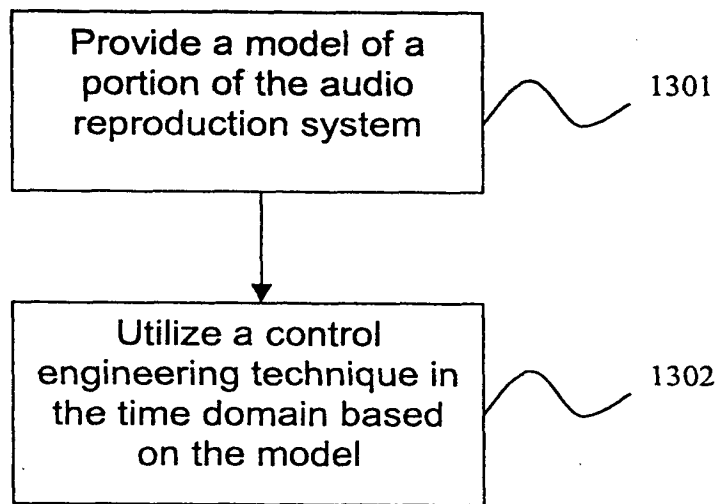


FIG. 8

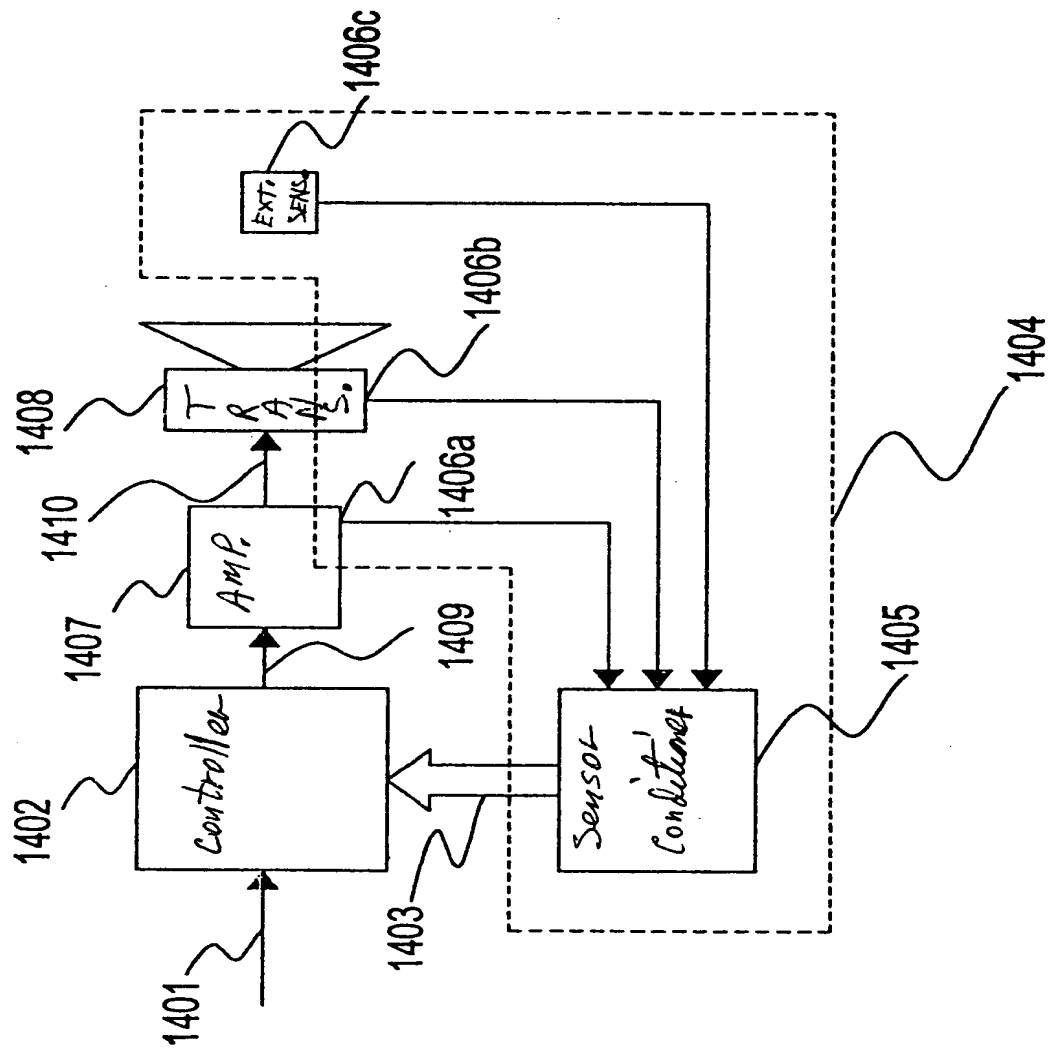


FIG. 9

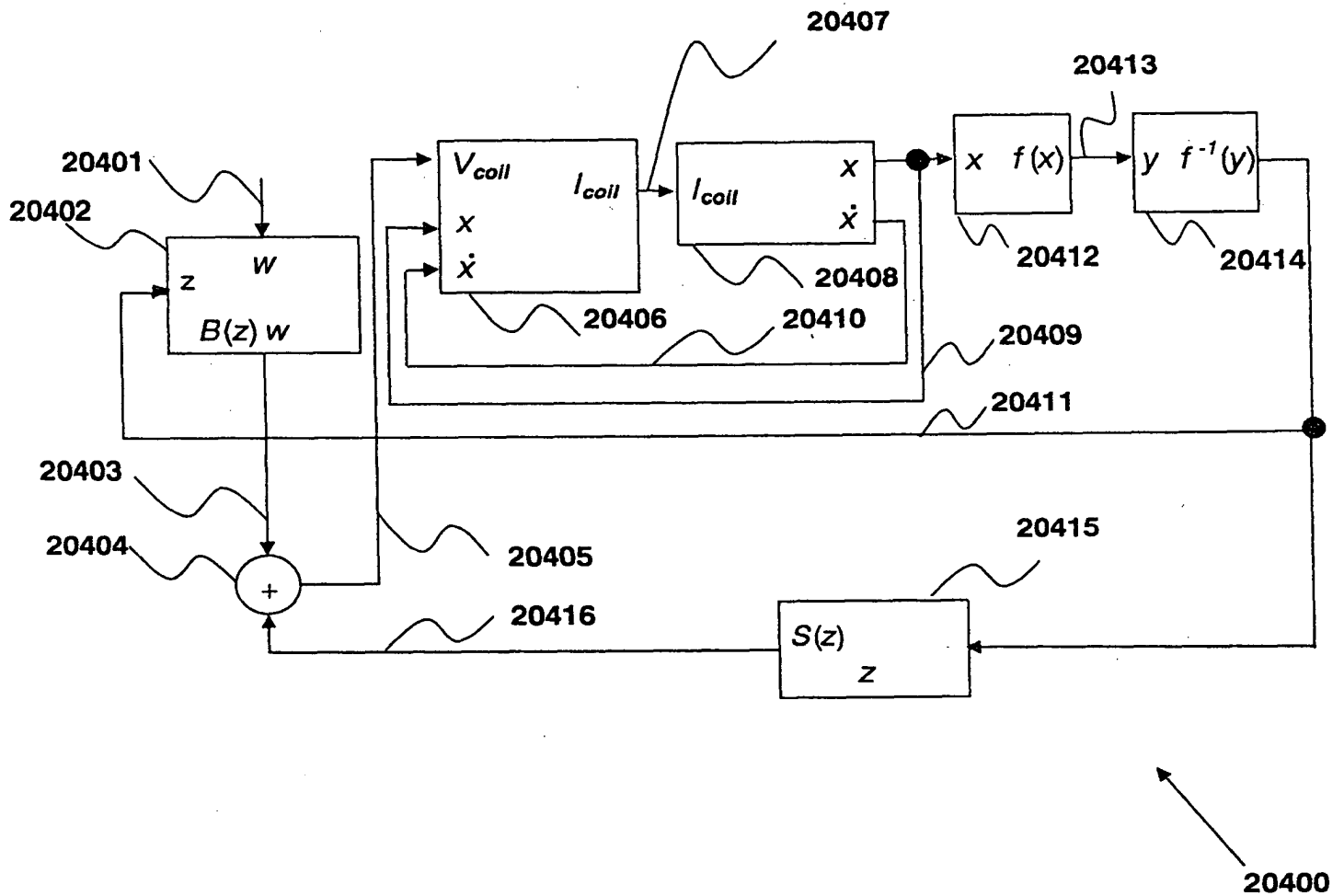


FIG. 10

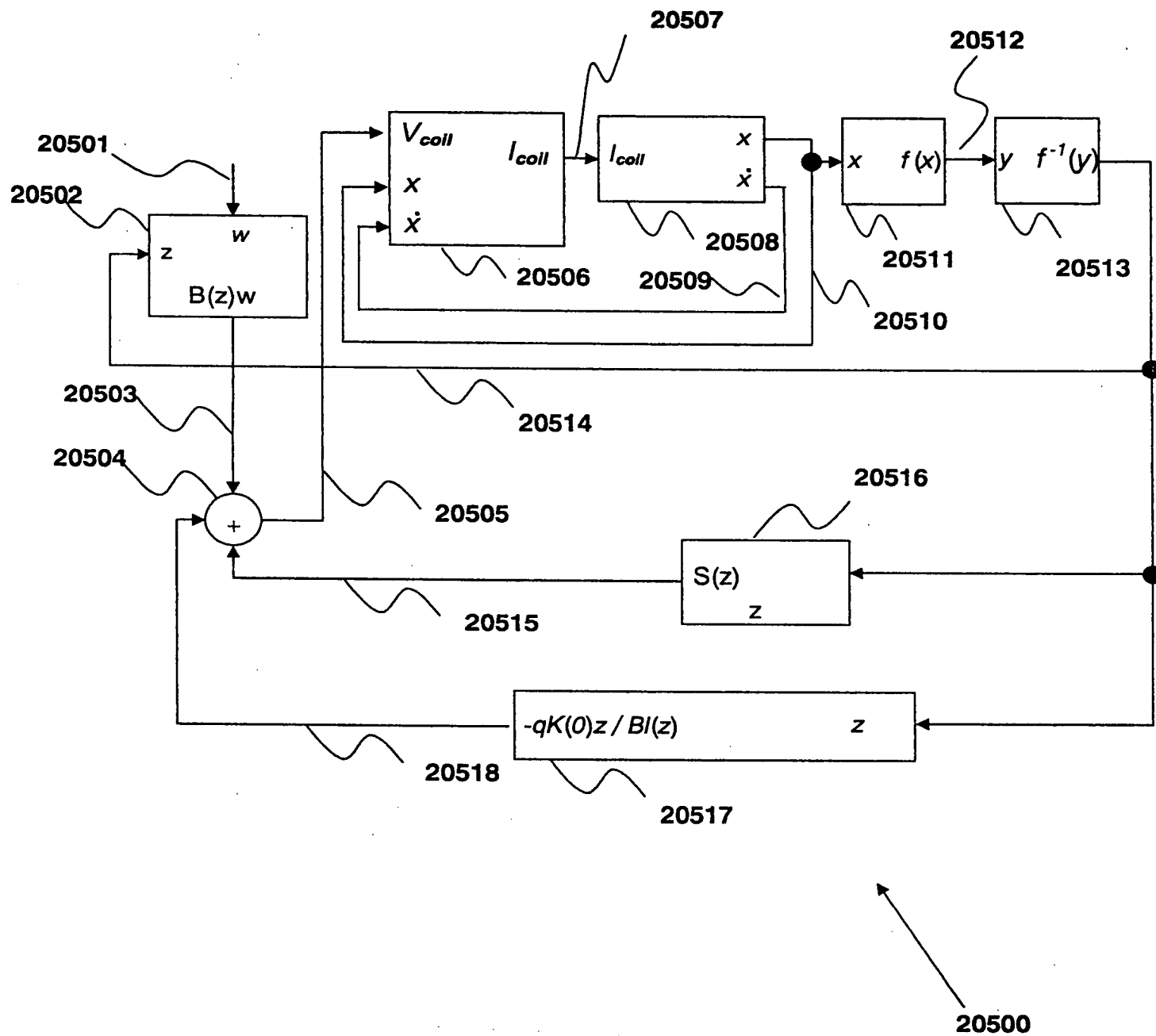


FIG. 11

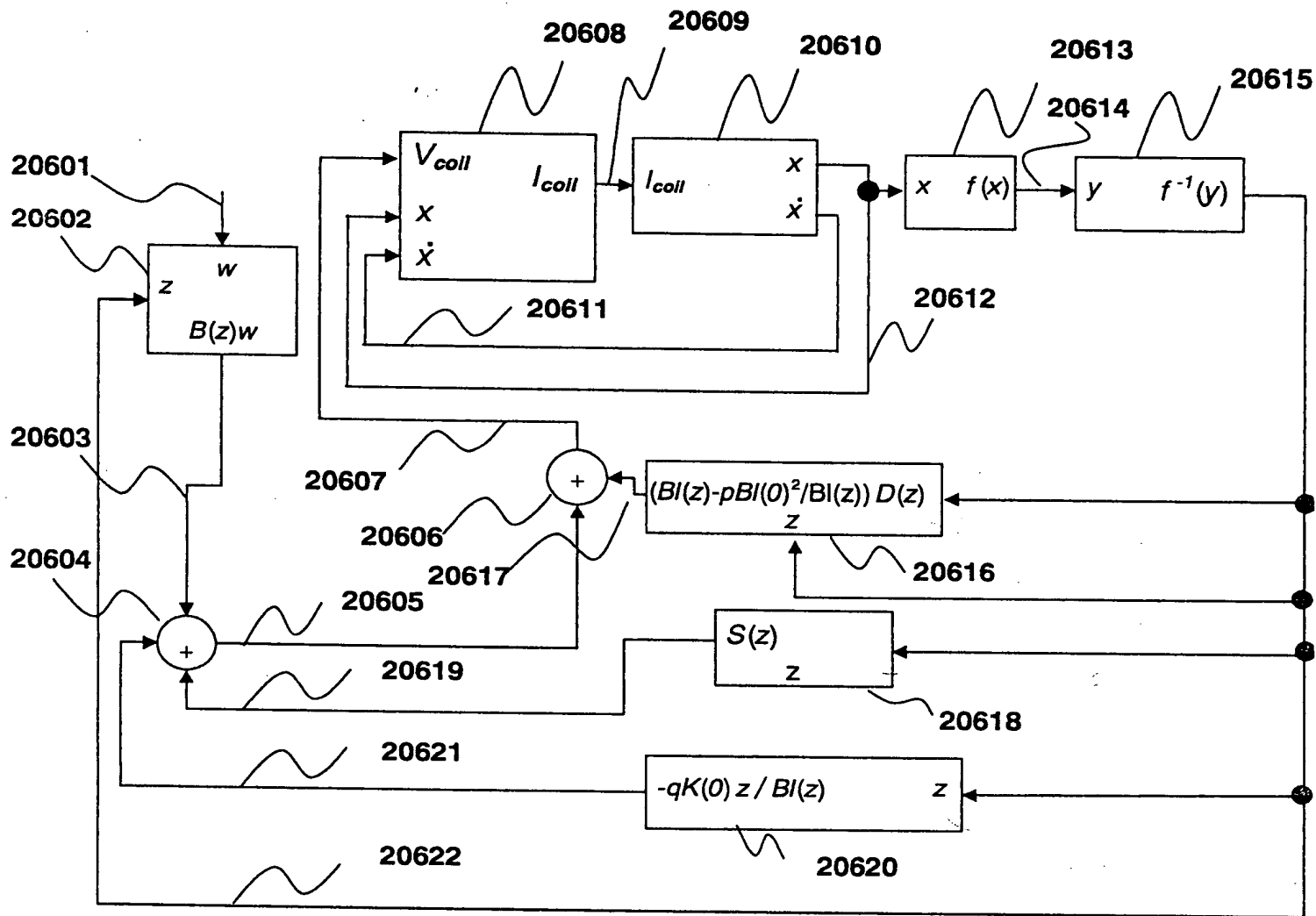


FIG. 12

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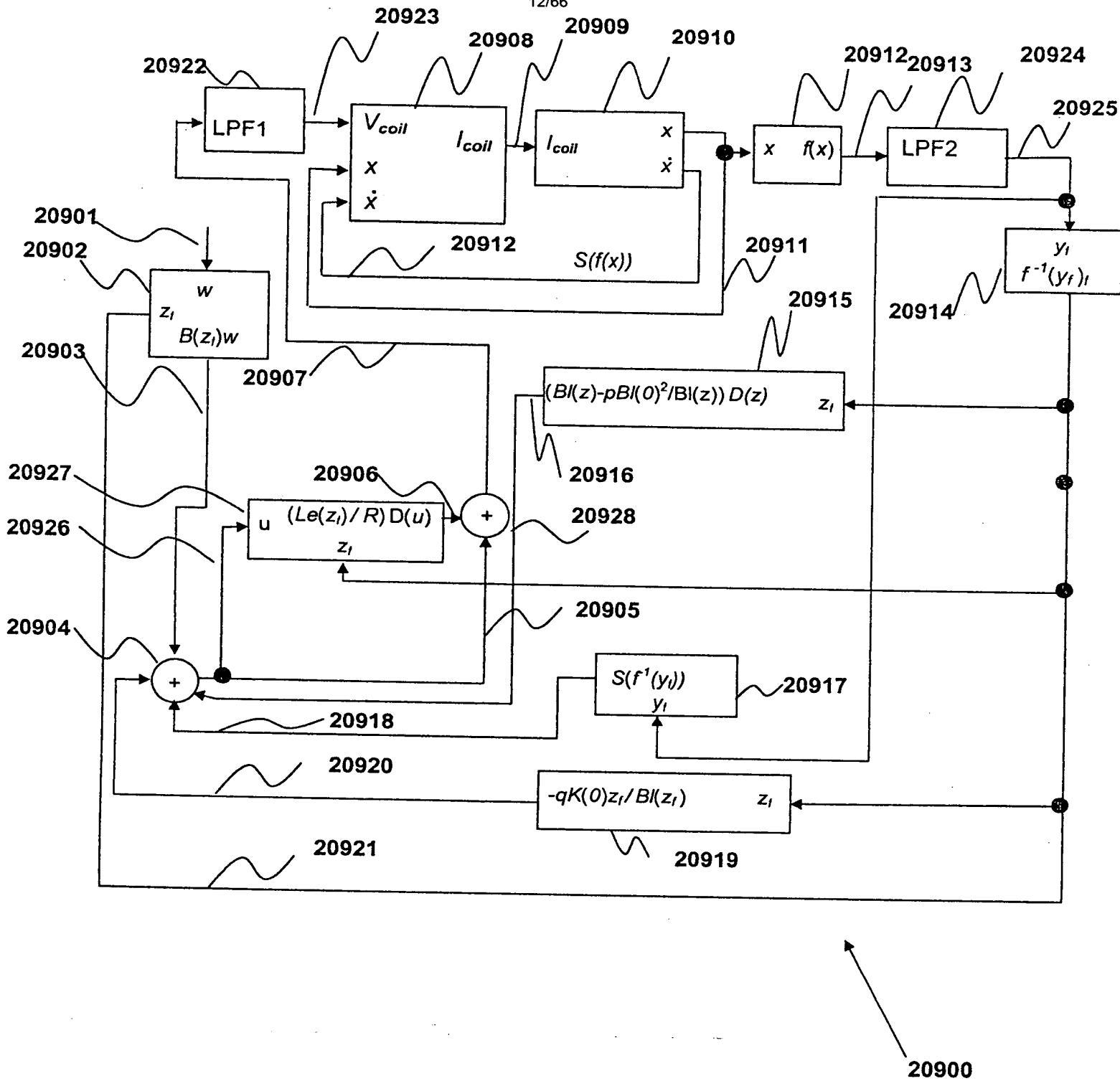


FIG. 13

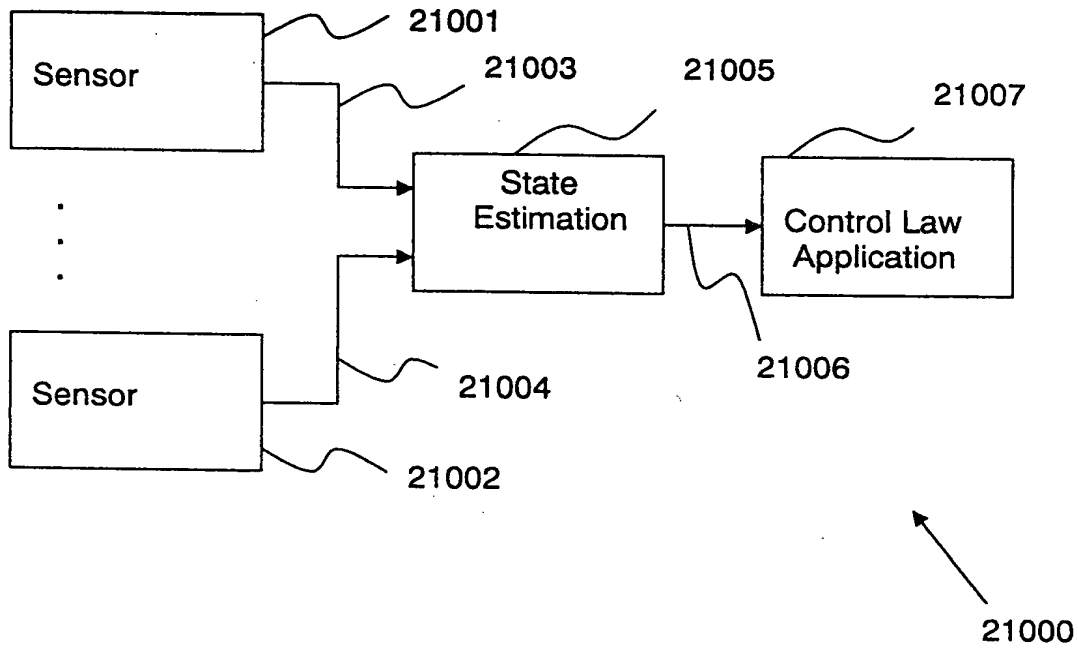


FIG. 14

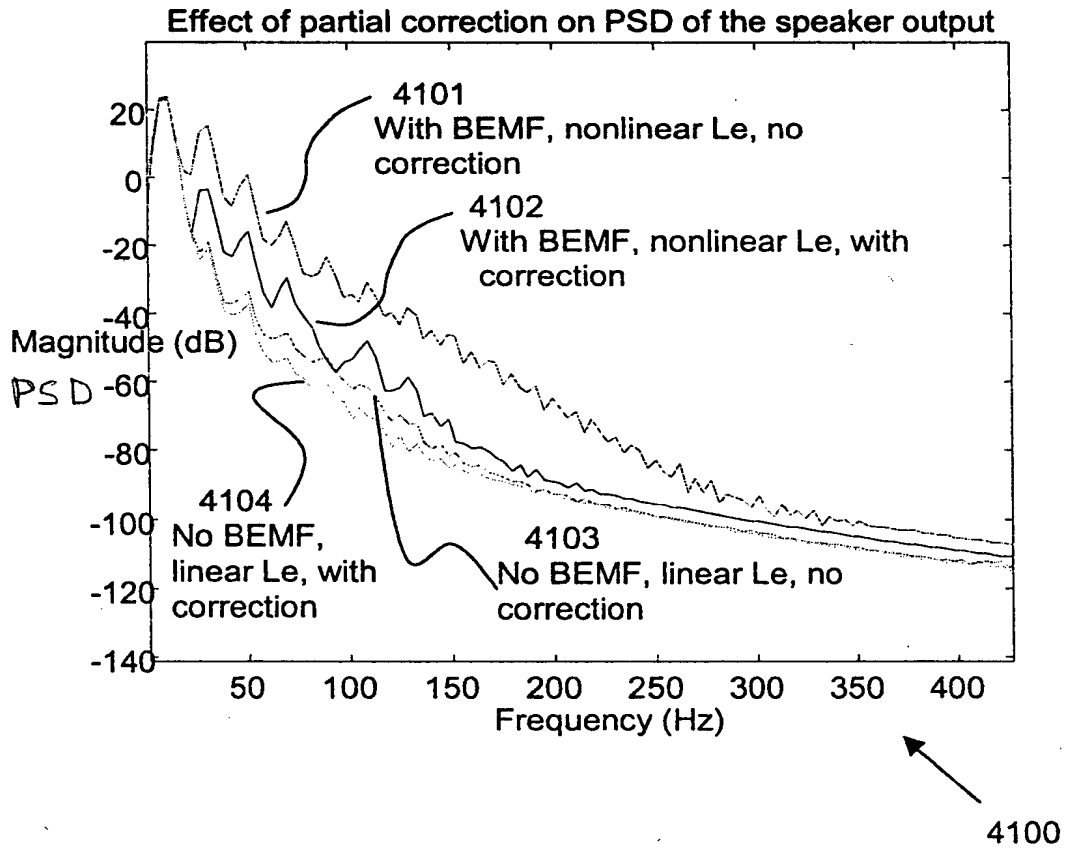
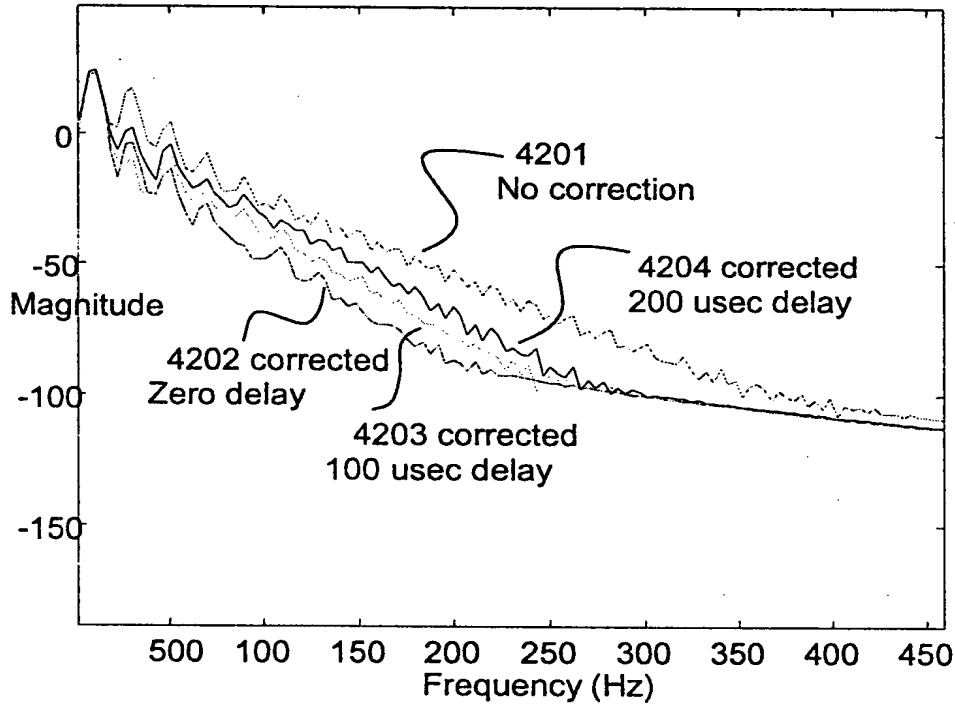


FIG. 15

PSD of the speaker output (cone velocity) with time-delayed correction



4200

FIG. 16

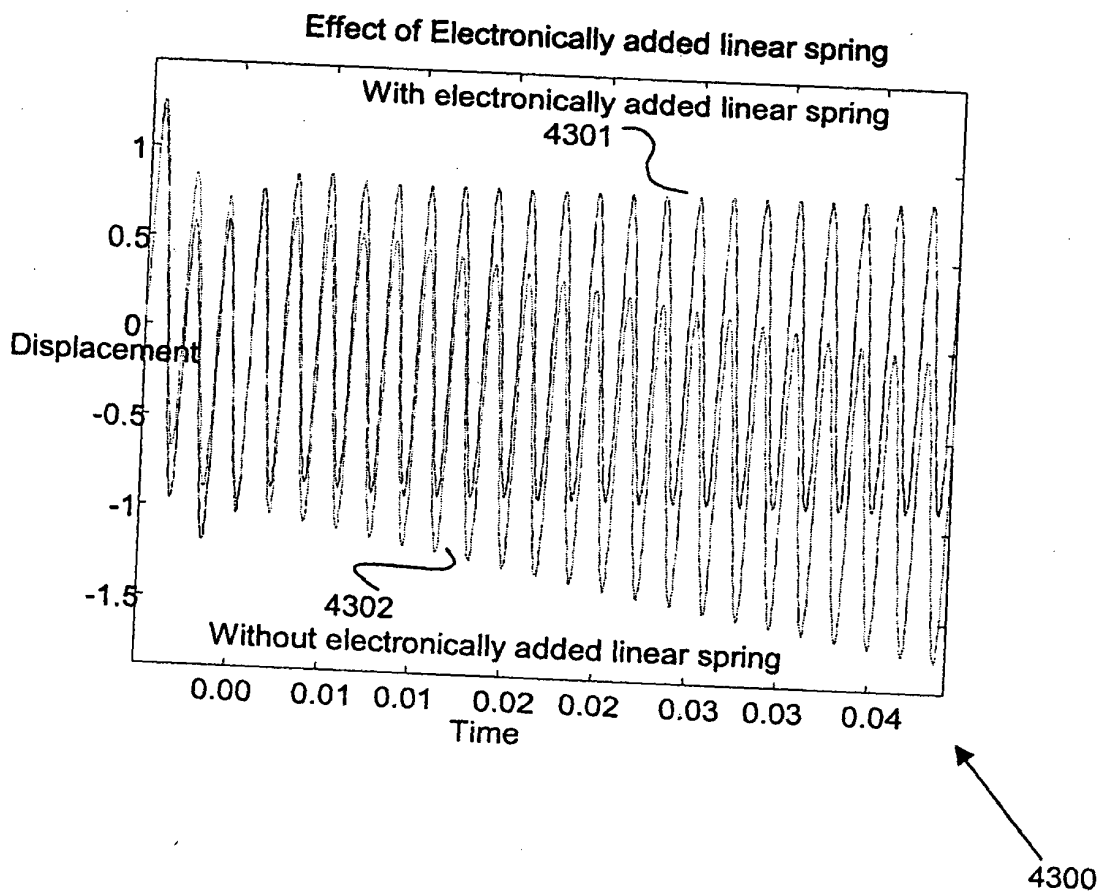


FIG. 17

Electronically implemented linear spring in the absence of $Bl(x)$ effect

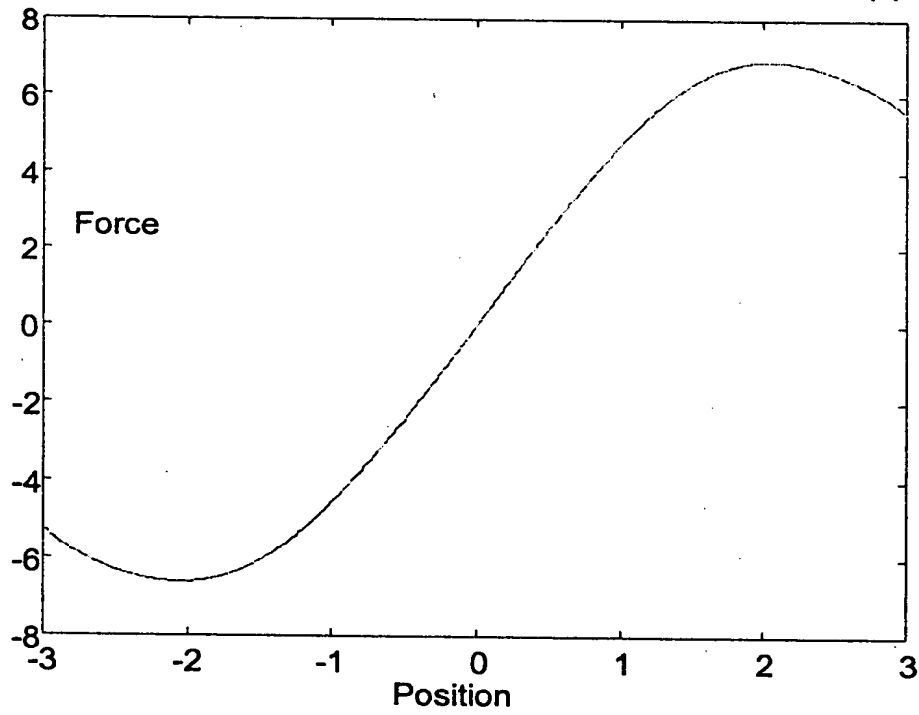


FIG. 18

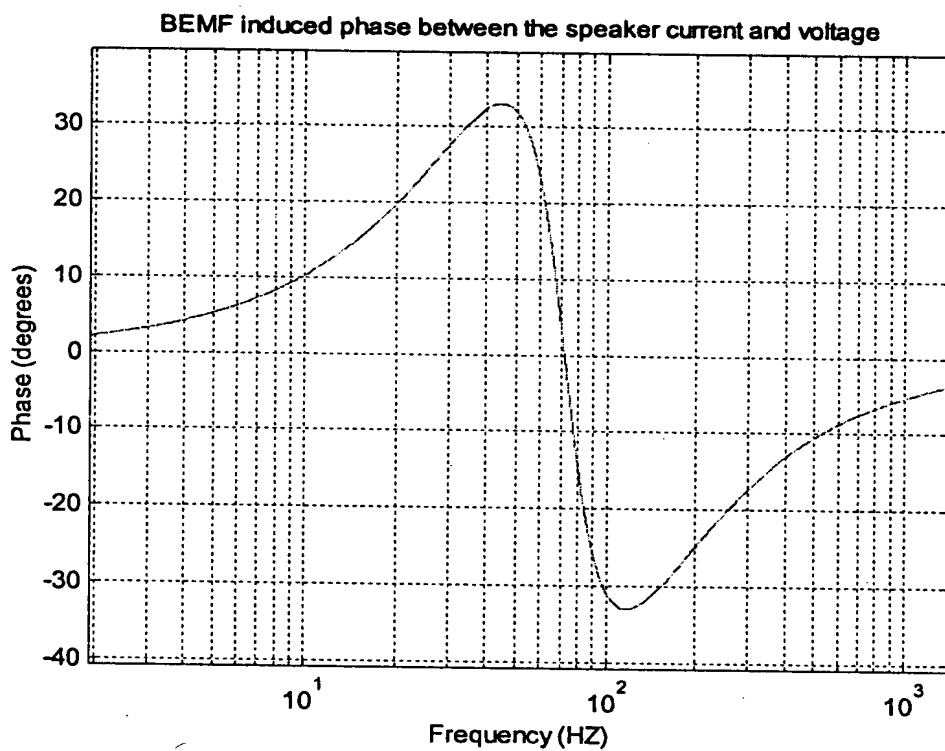


FIG. 19

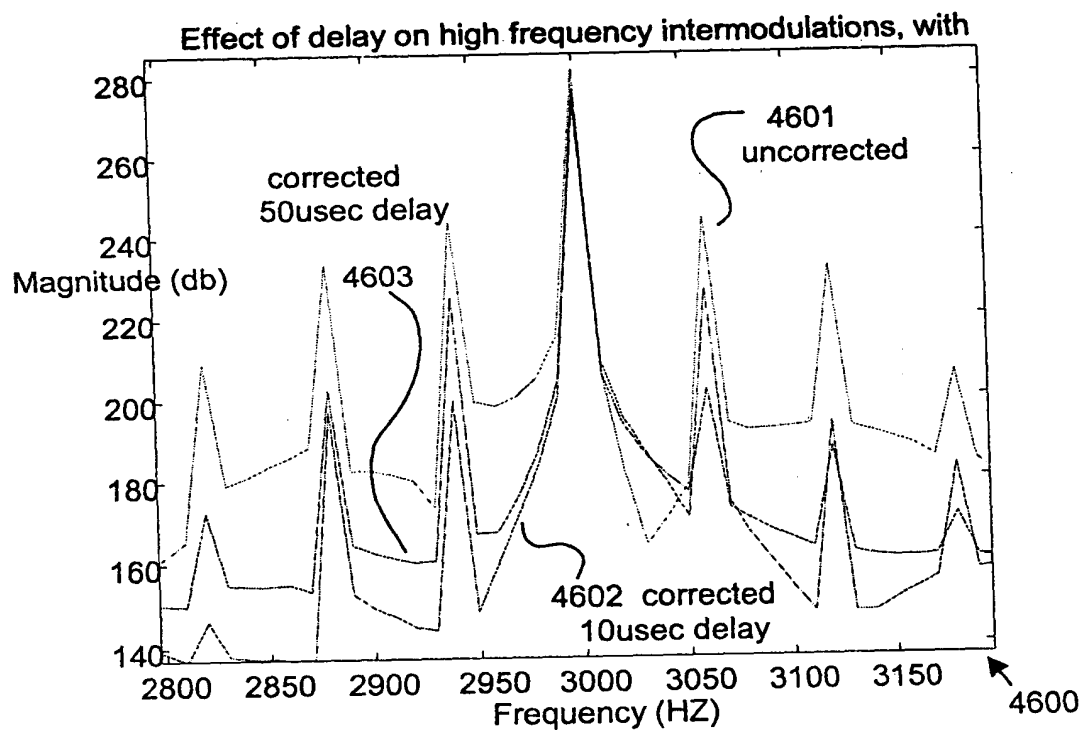


FIG. 20

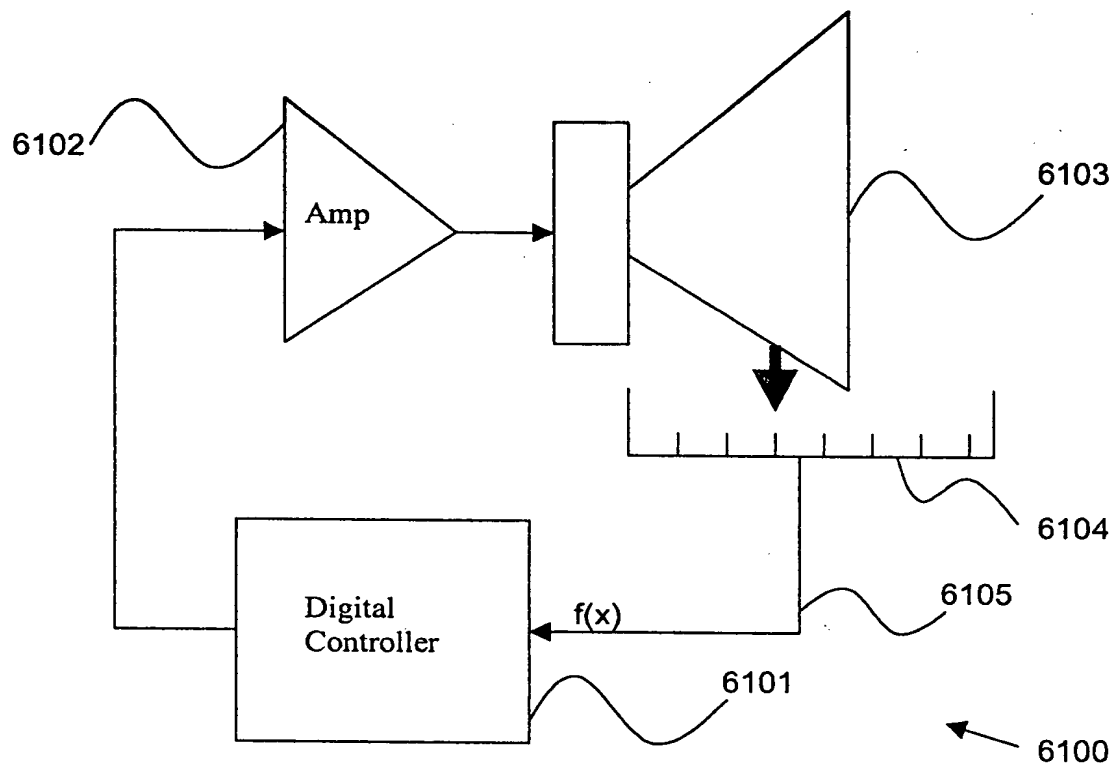


FIG. 21

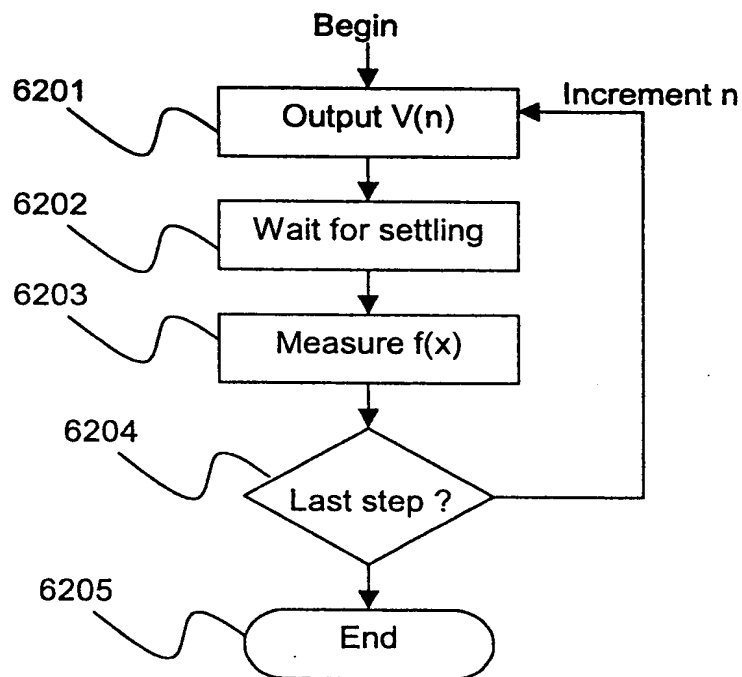


FIG. 22

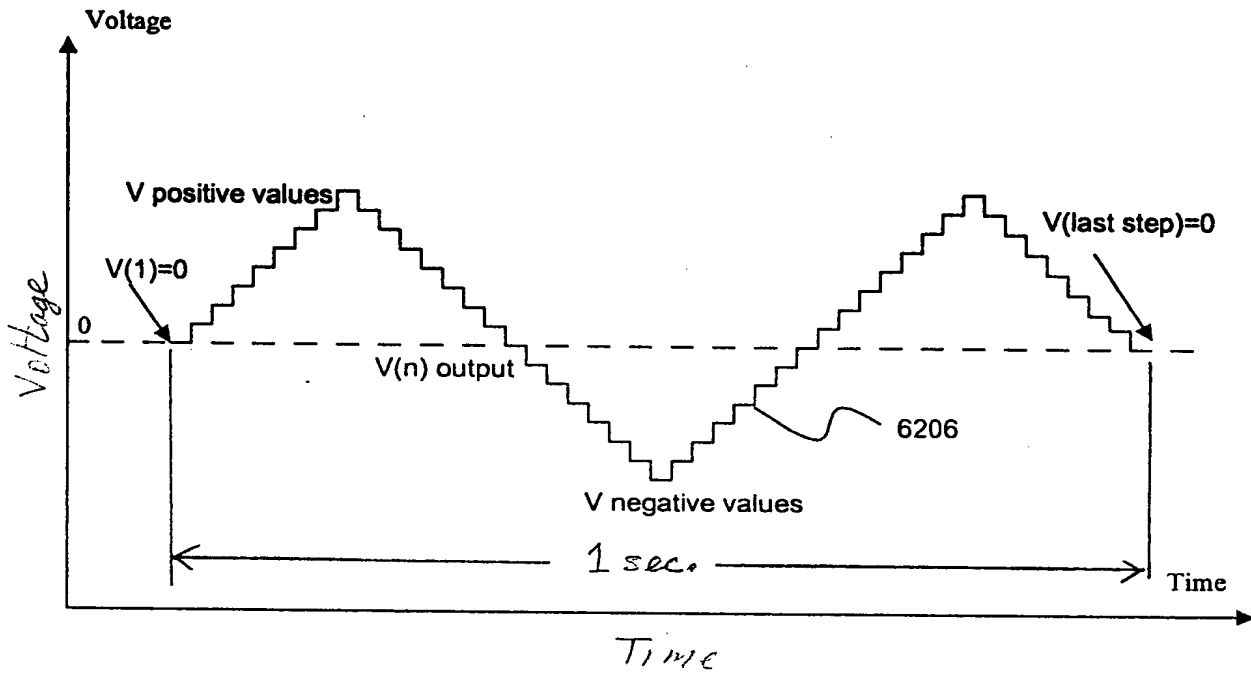


FIG. 23

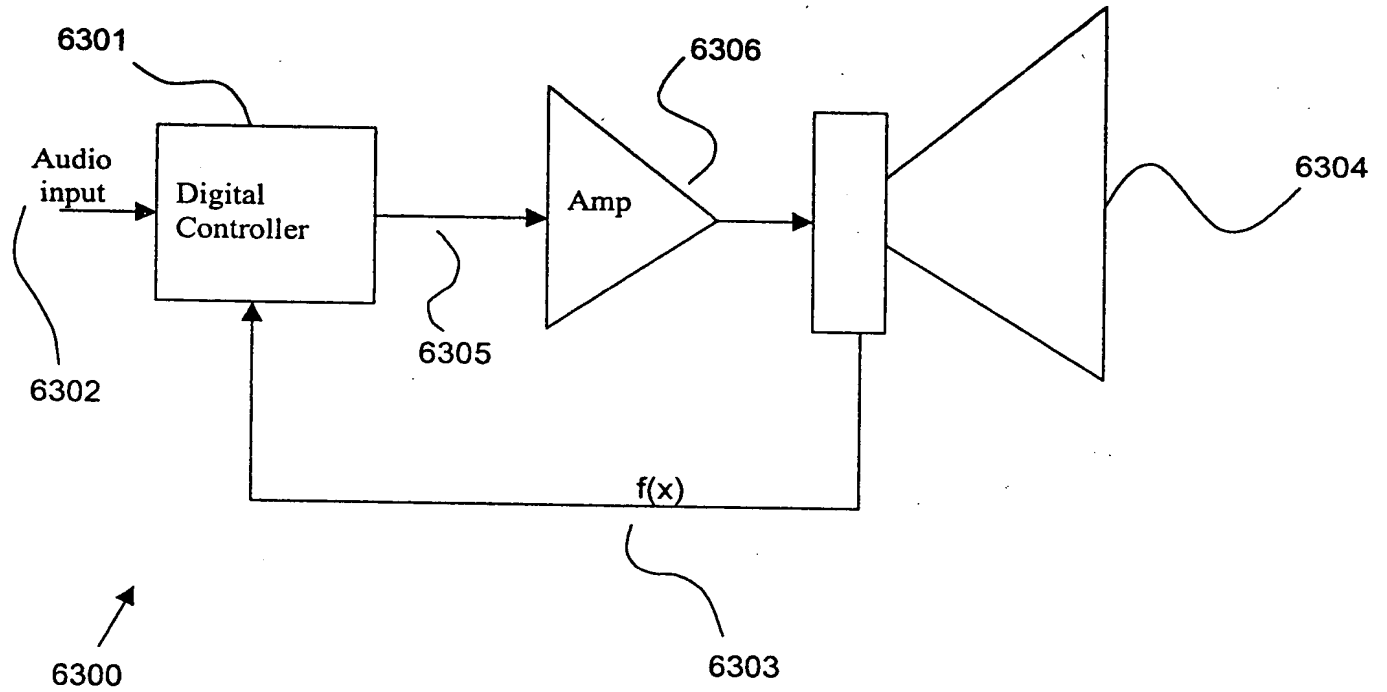


FIG. 24

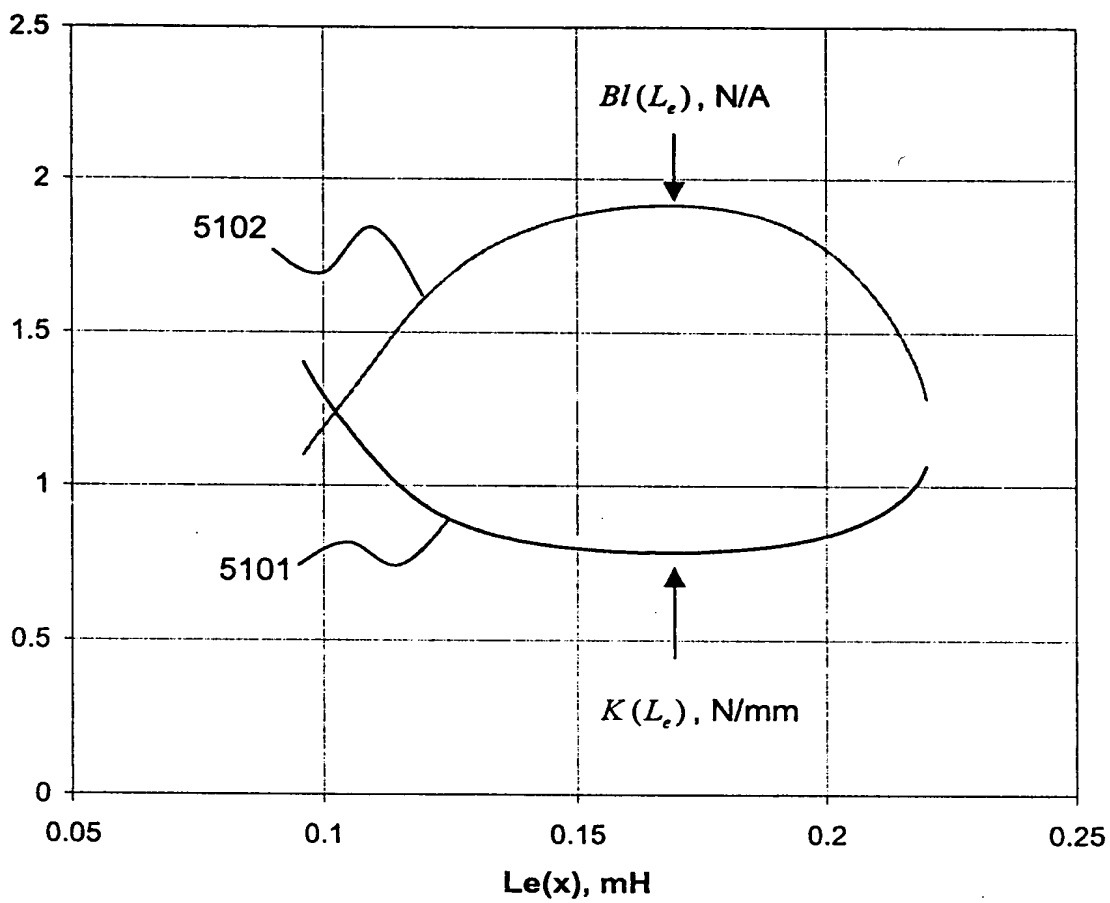


FIG. 25

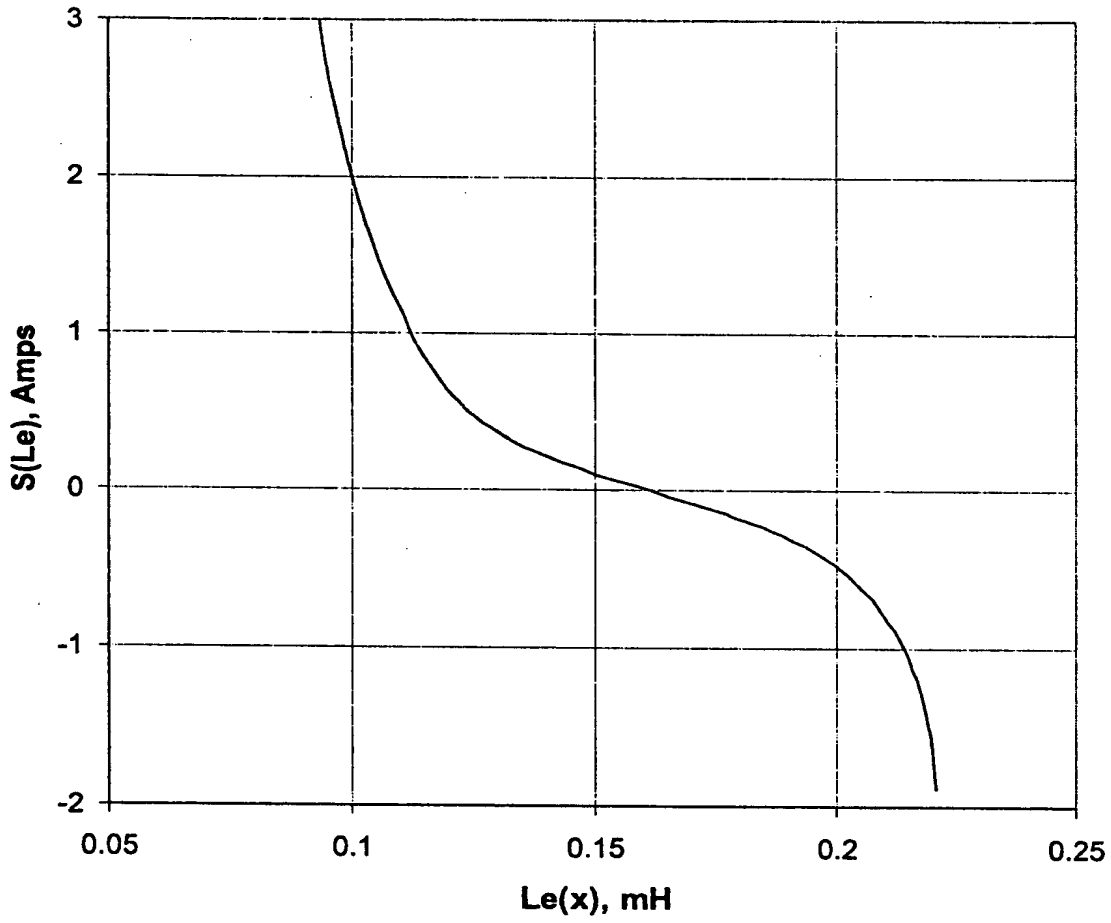


FIG. 26

L_e vs. Coil Displacement at 43 kHz

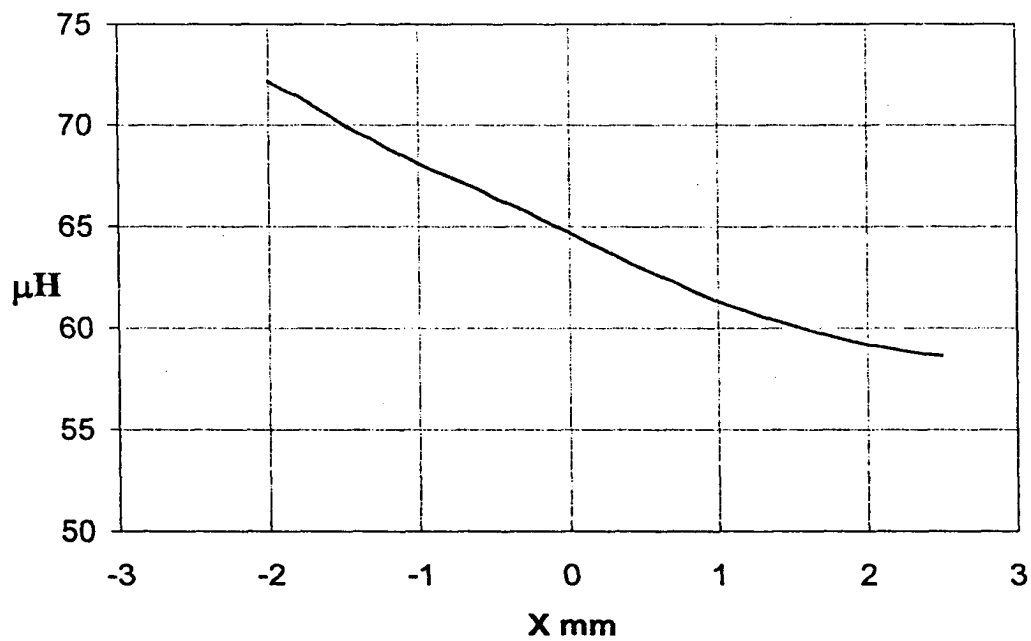


FIG. 27

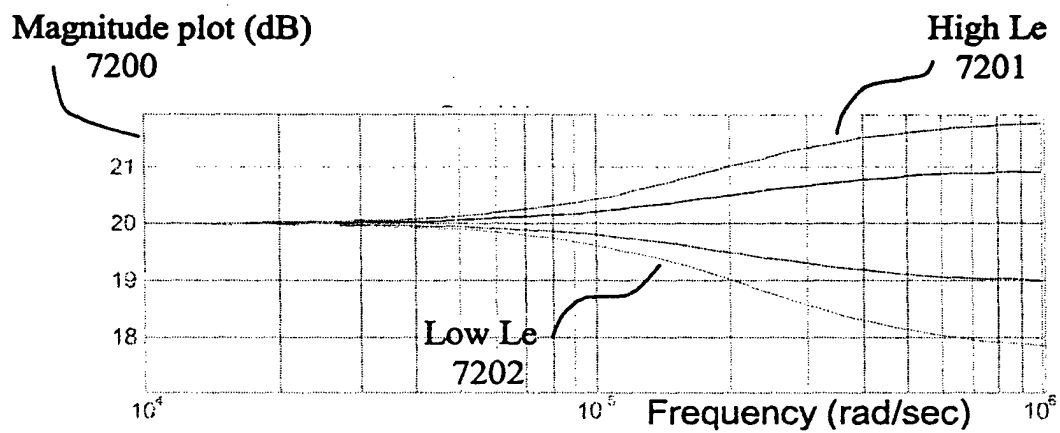


FIG. 28

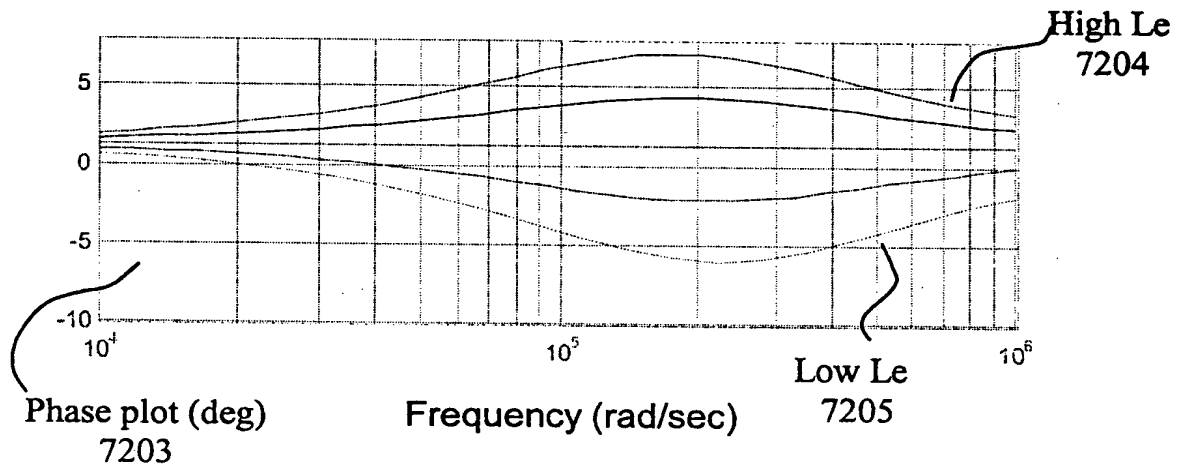


FIG. 29

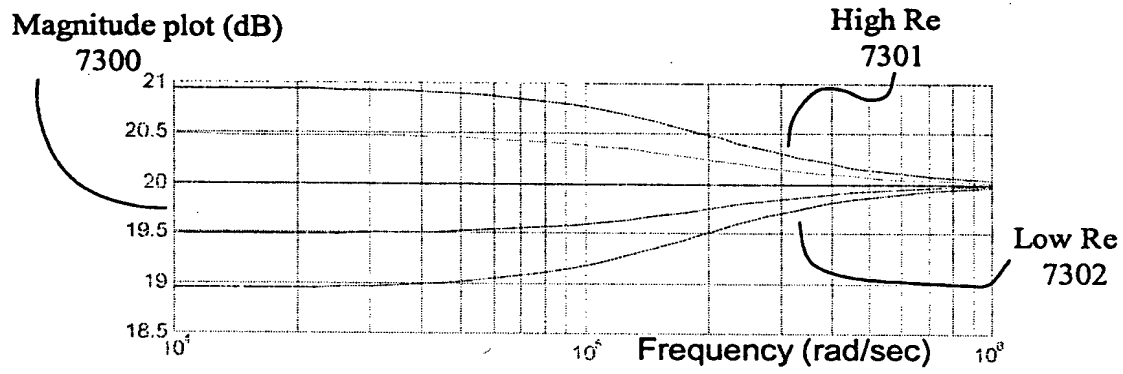


FIG. 30

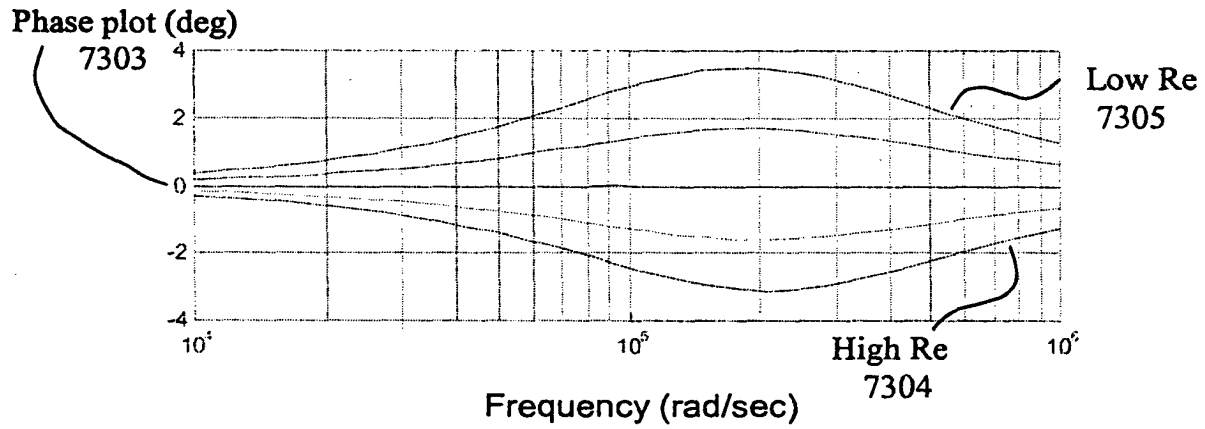


FIG. 31

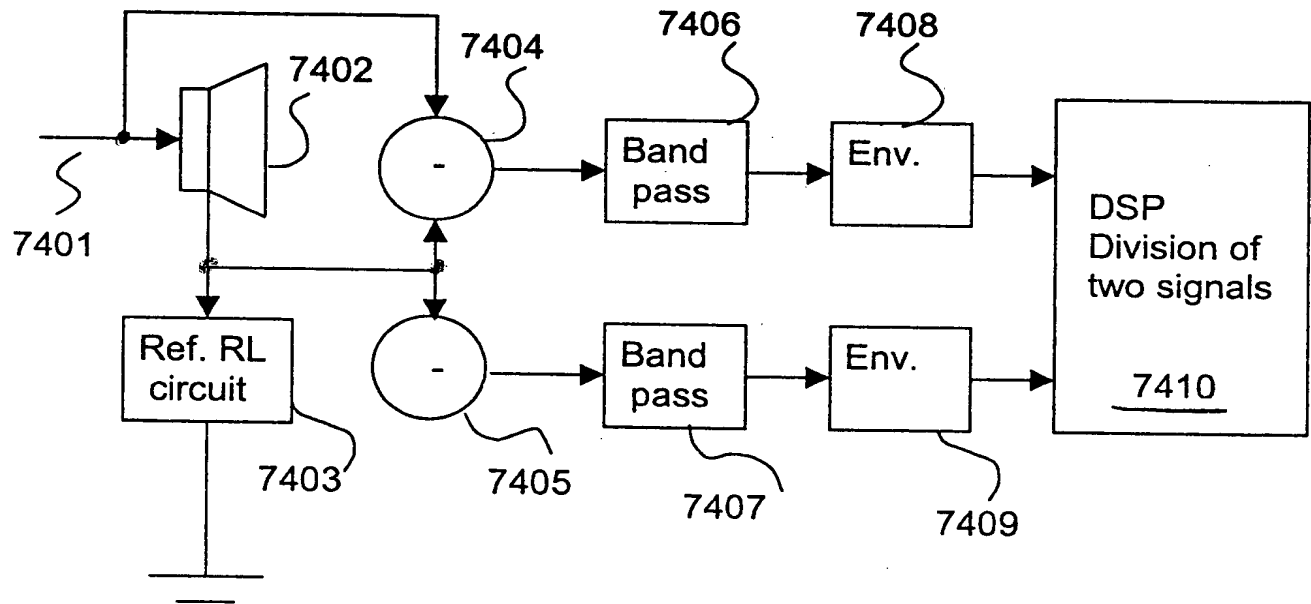


FIG. 32

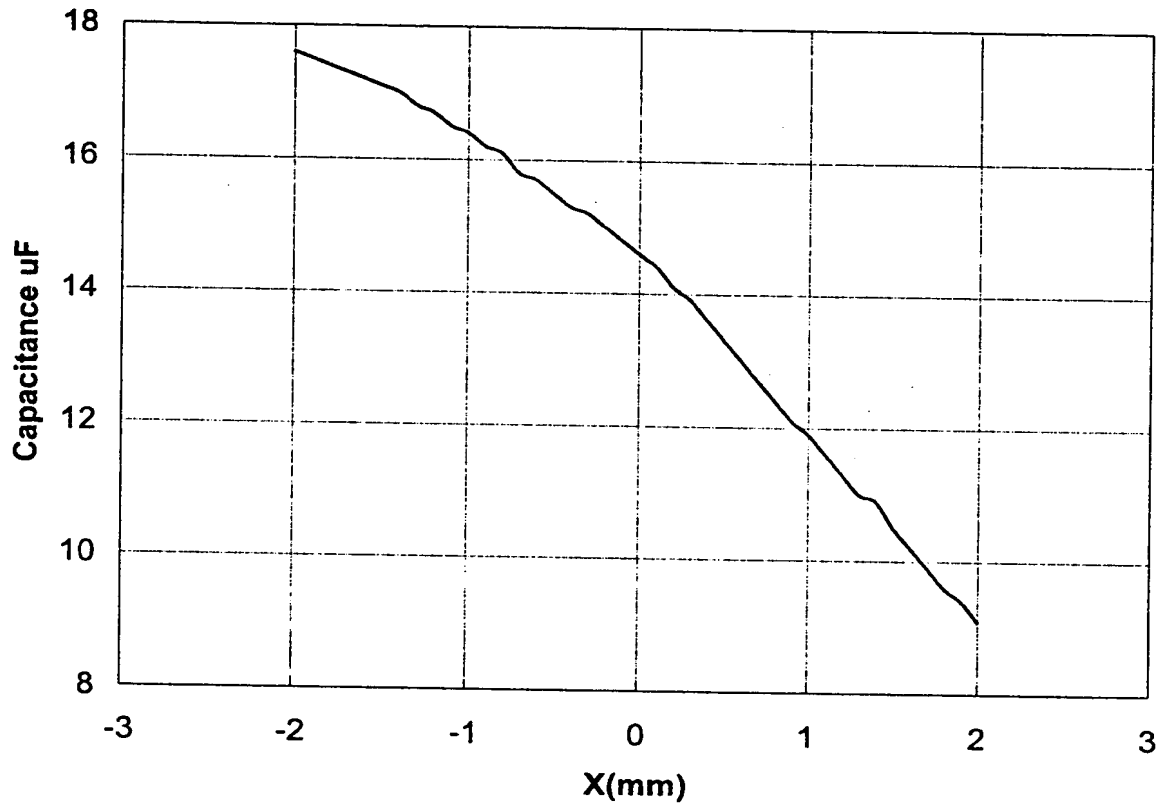


FIG. 33

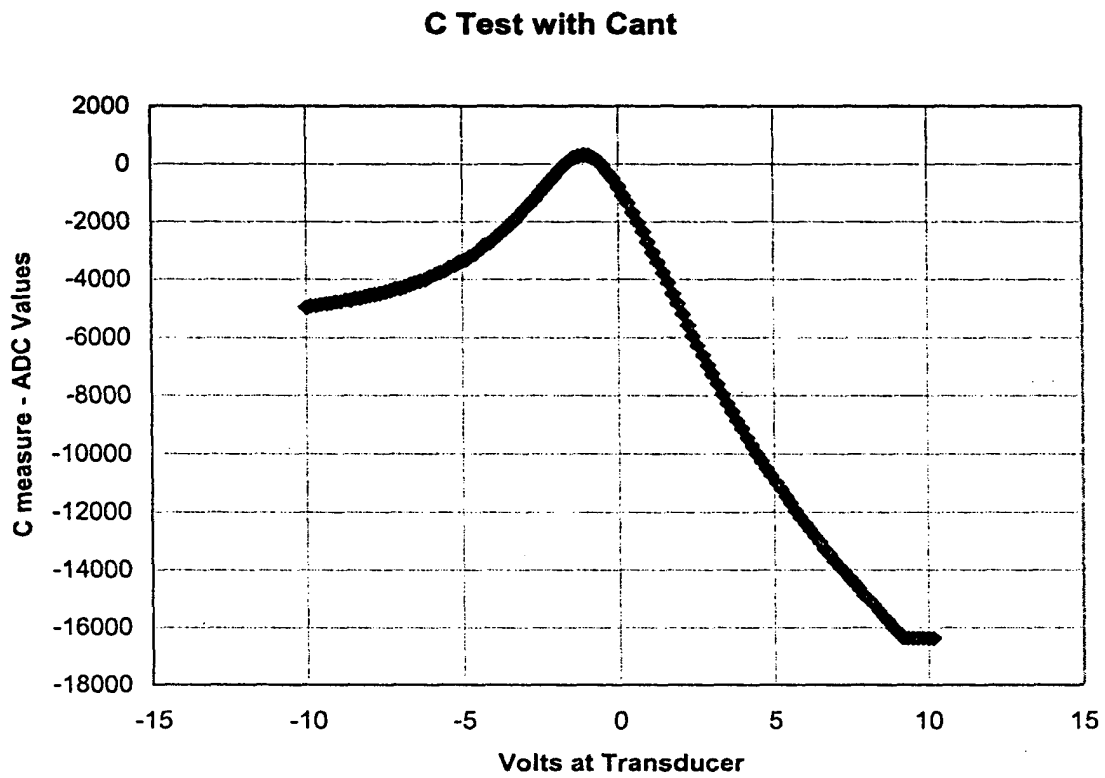


FIG. 34

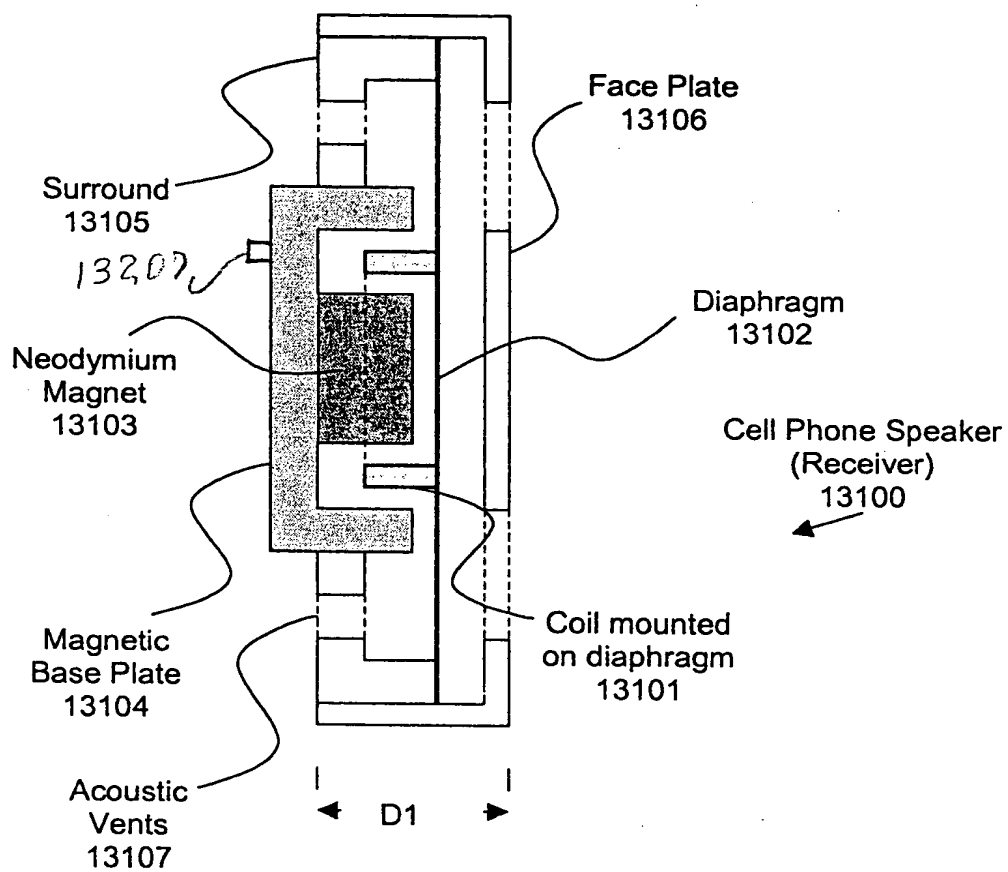


FIG. 35

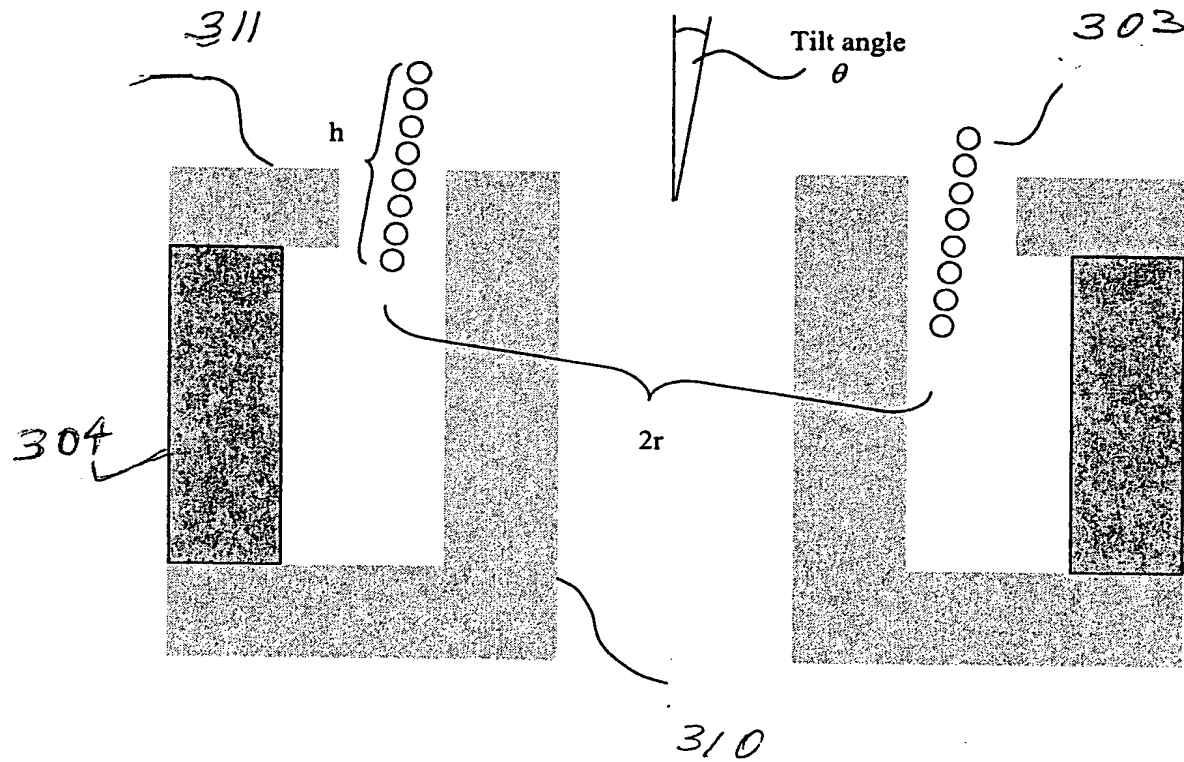


FIG. 36

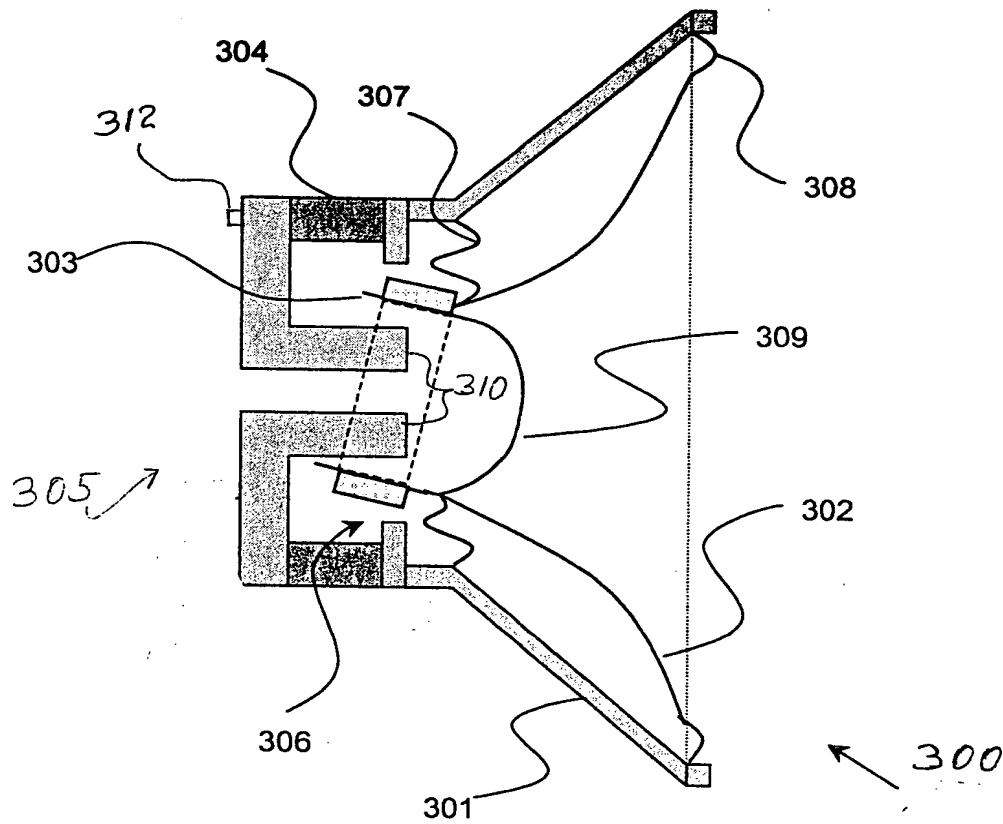
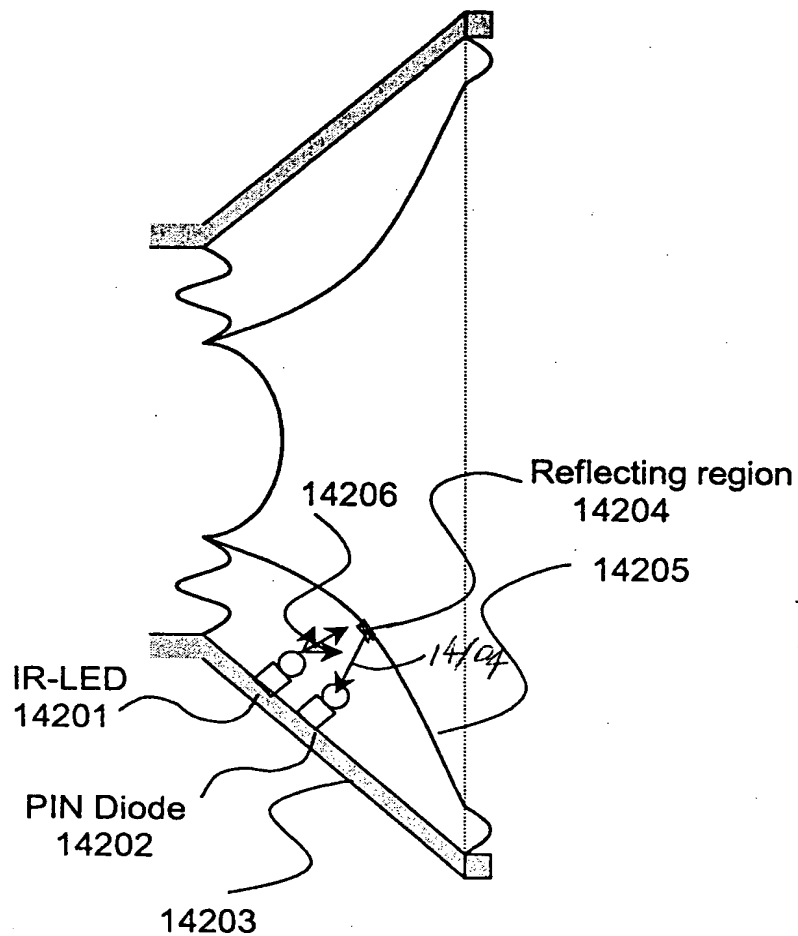


FIG. 37



14200 ↗

FIG. 38

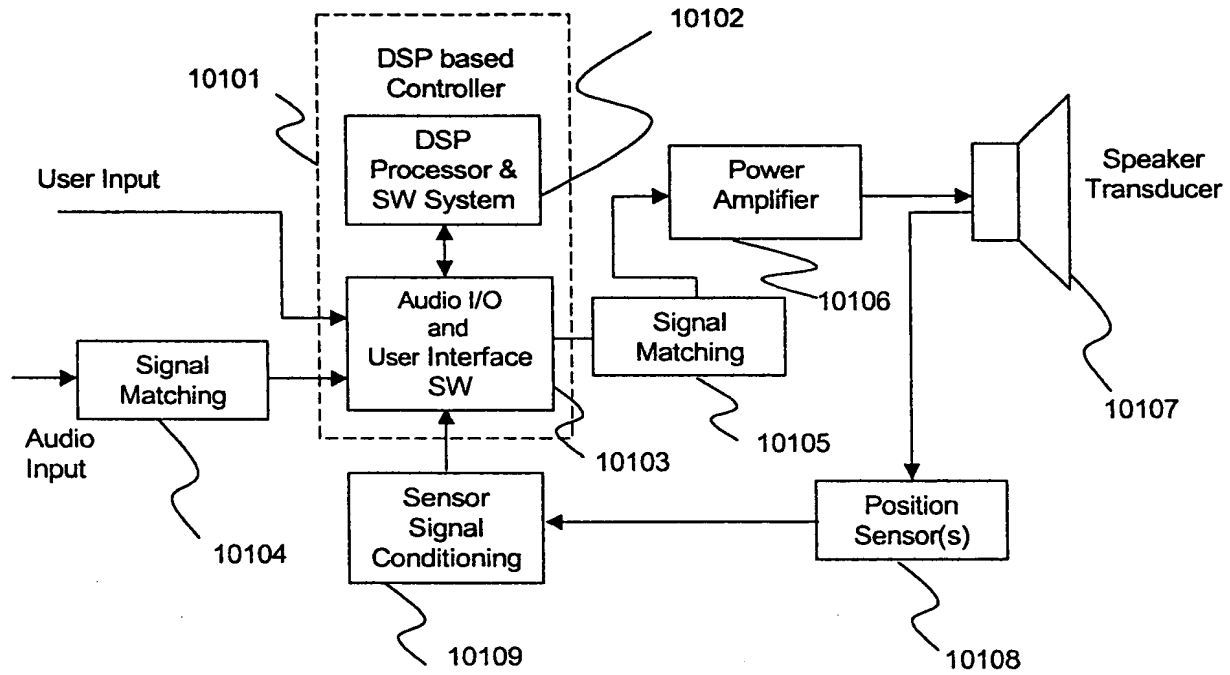


FIG. 39

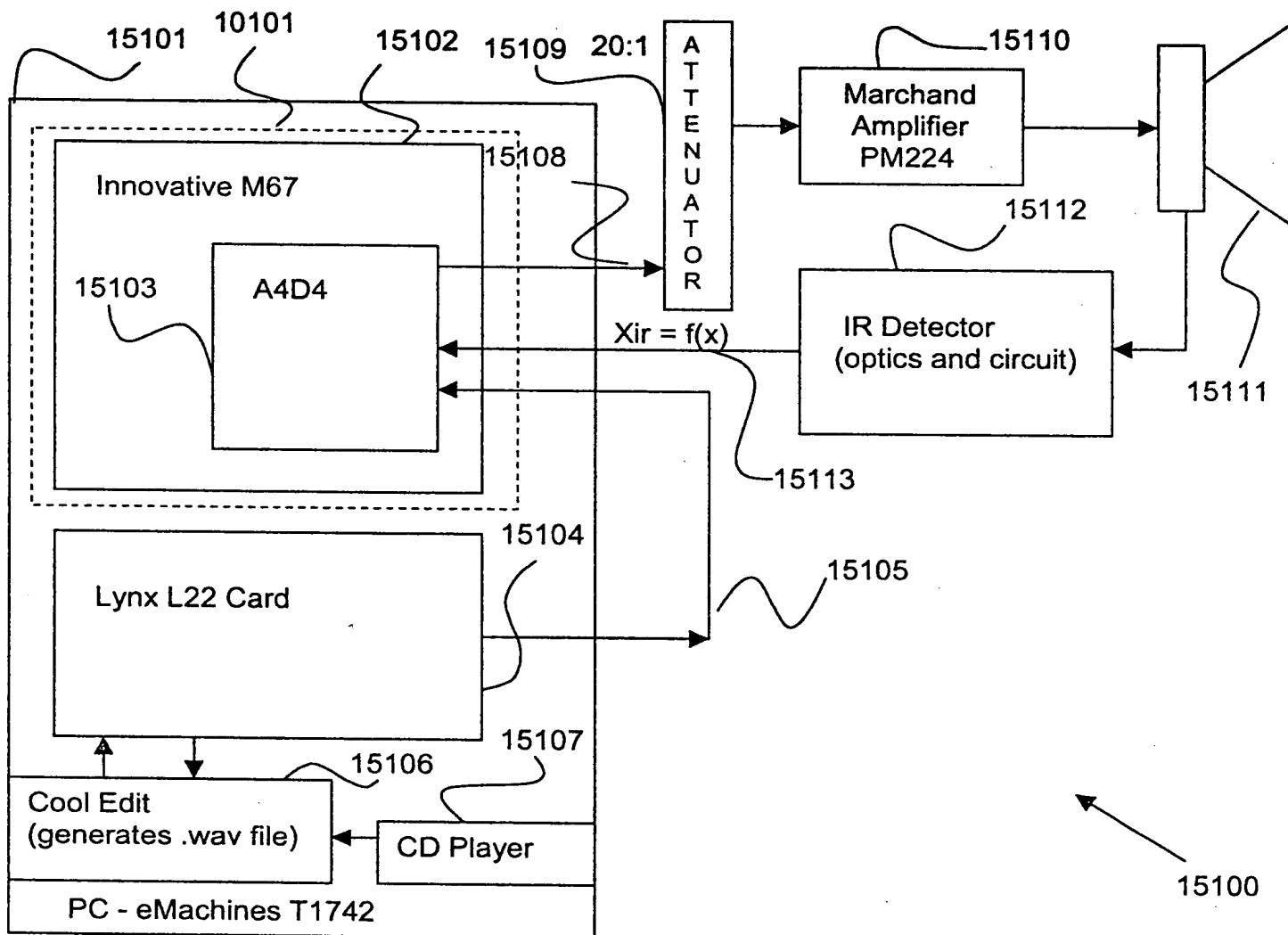
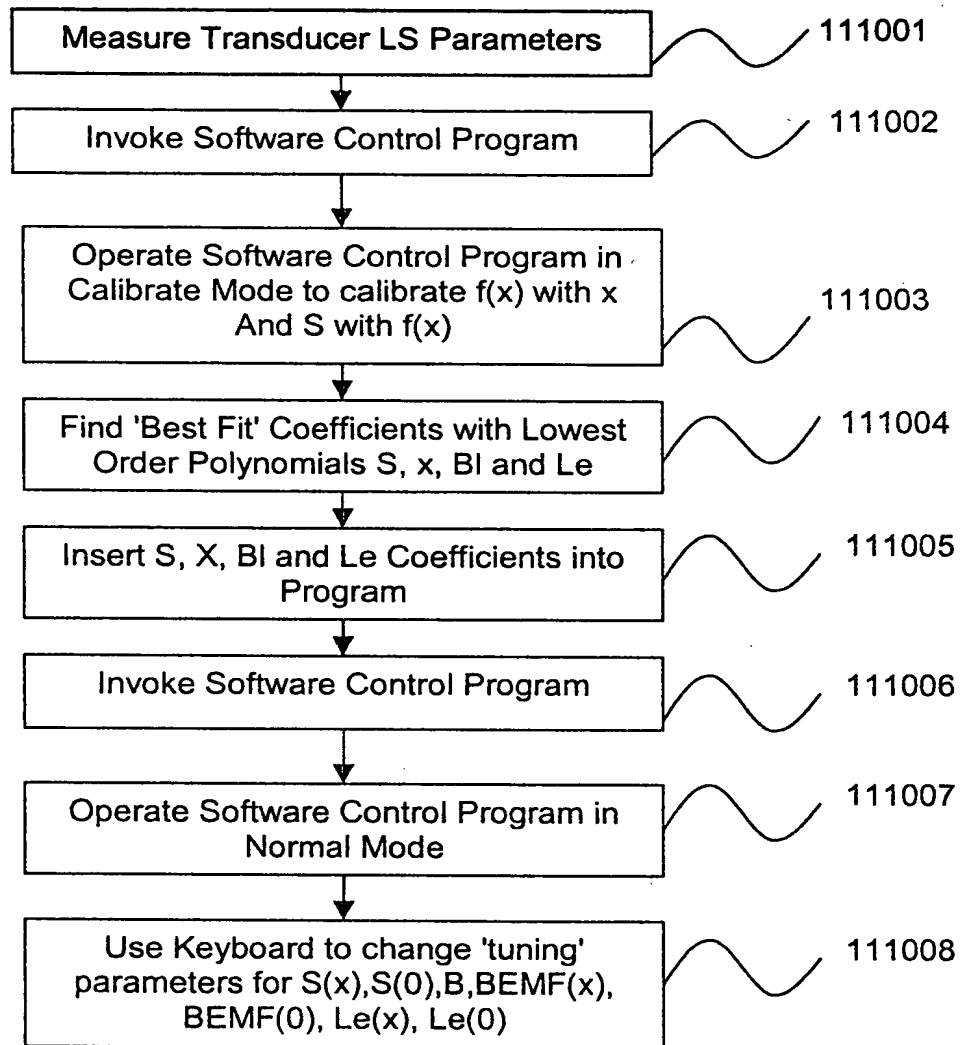
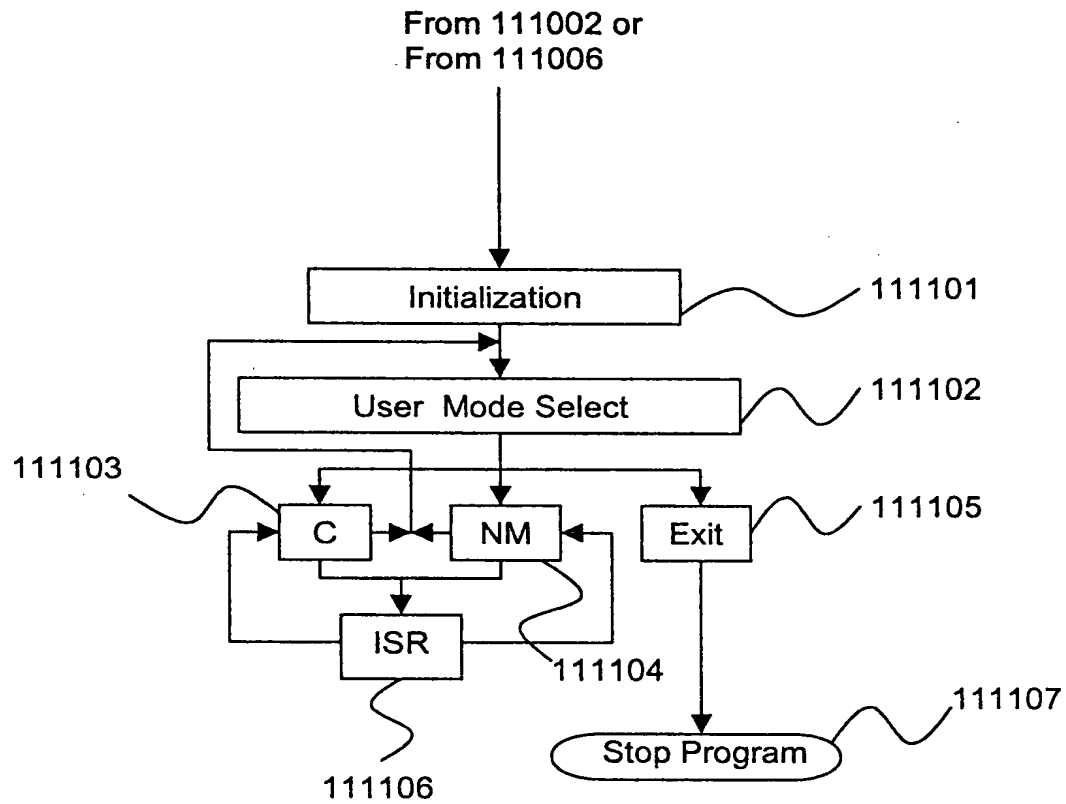


FIG. 40



LS = Large Signal

FIG. 41



NM: Normal Mode
C: Calibration mode

FIG. 42

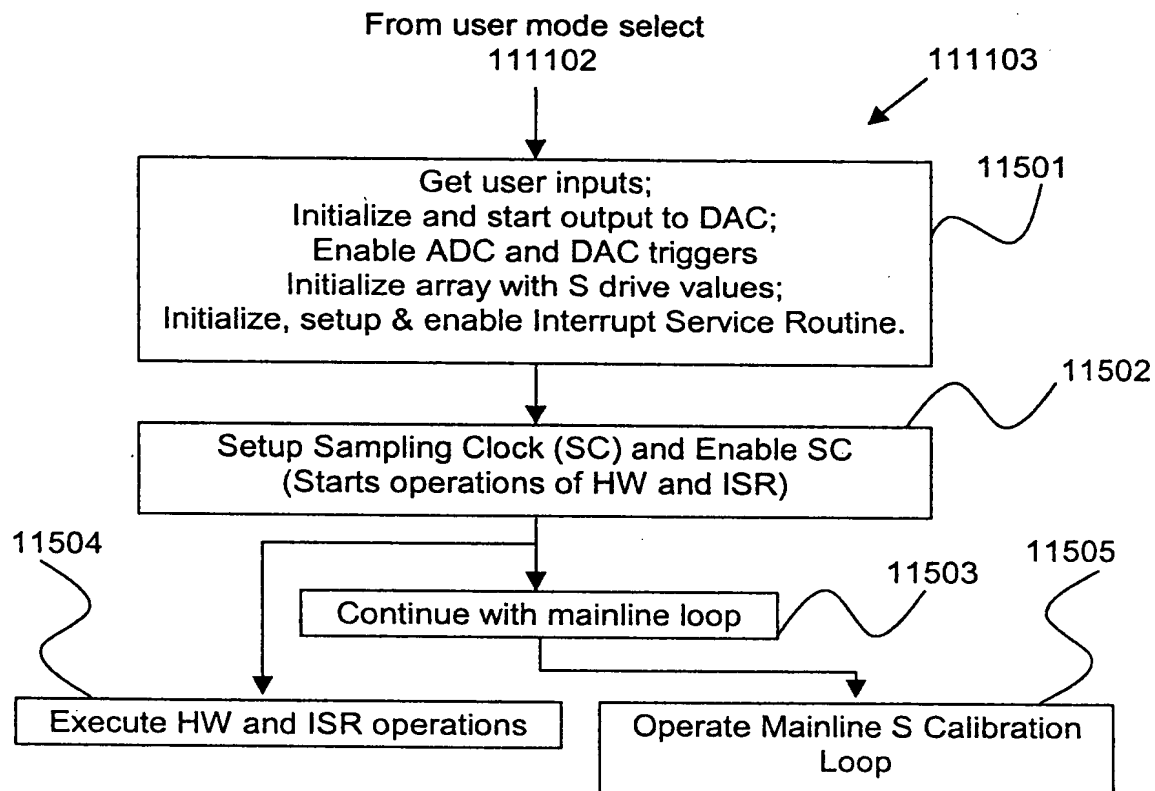


FIG. 43

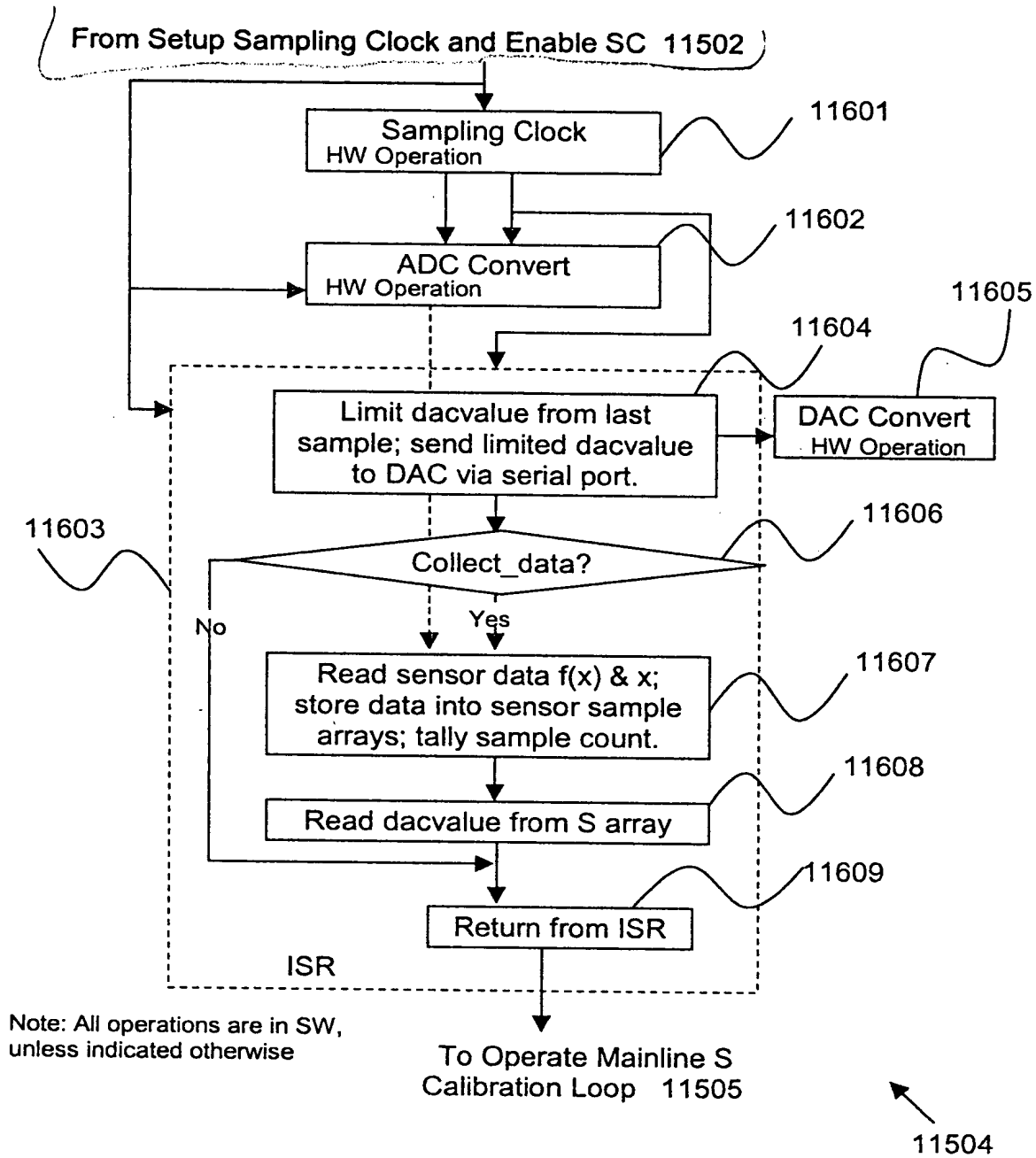
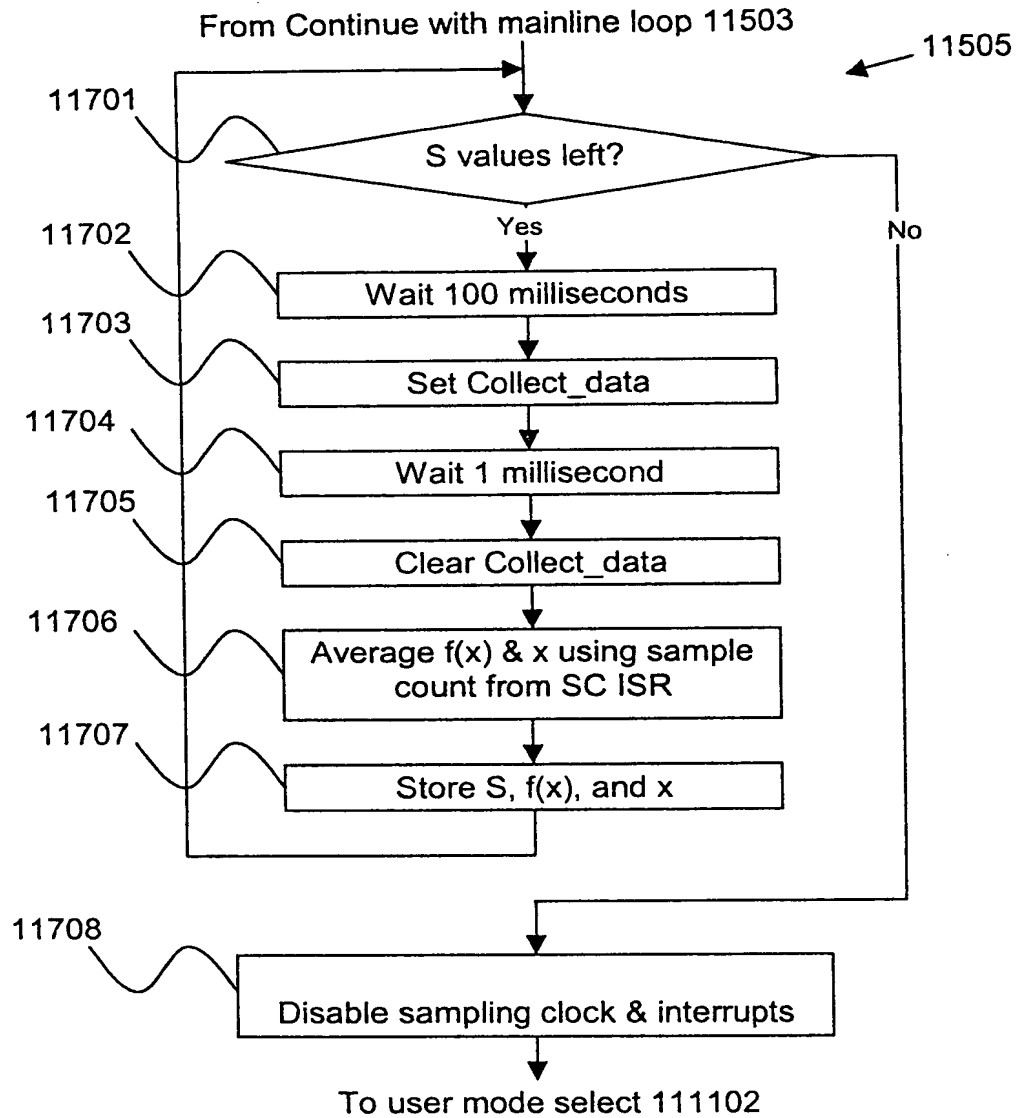


FIG. 44



All operations are in SW

FIG. 45

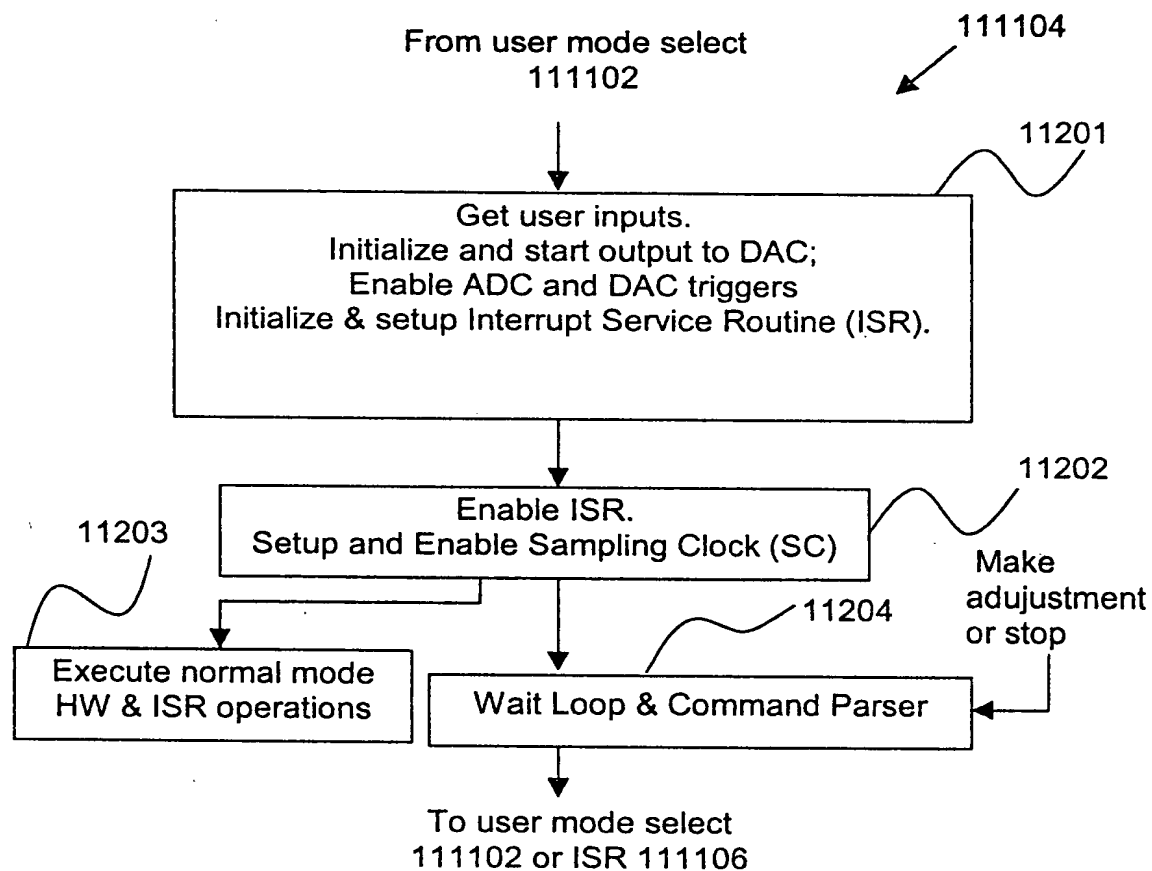


FIG. 46

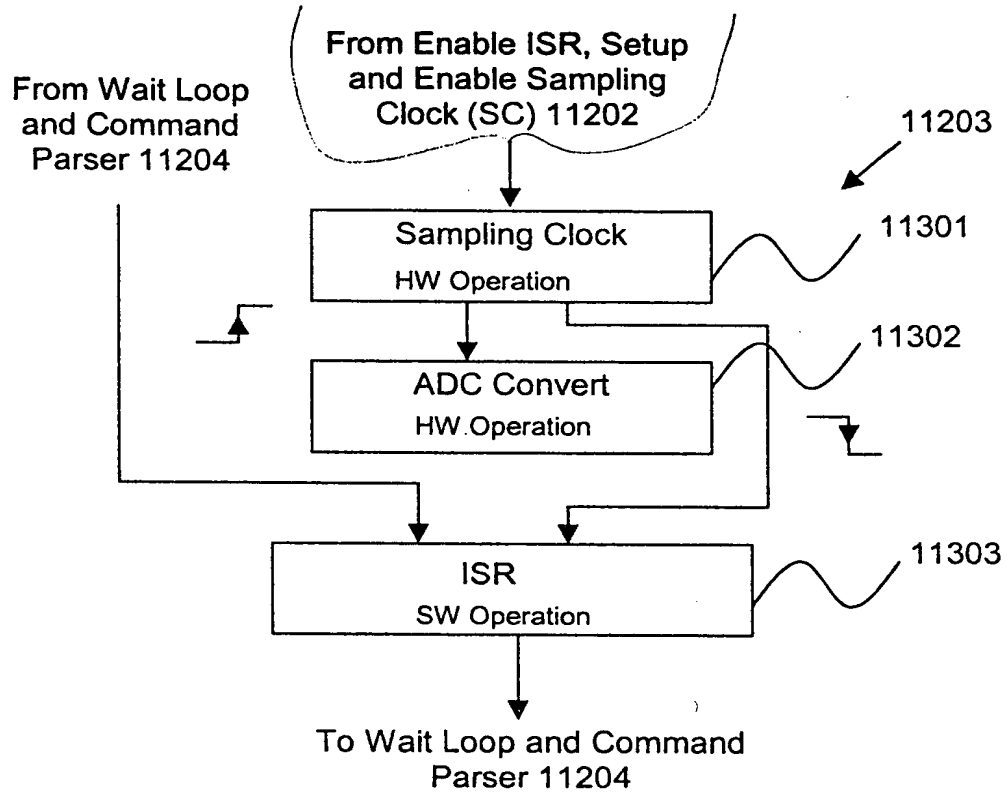


FIG. 47

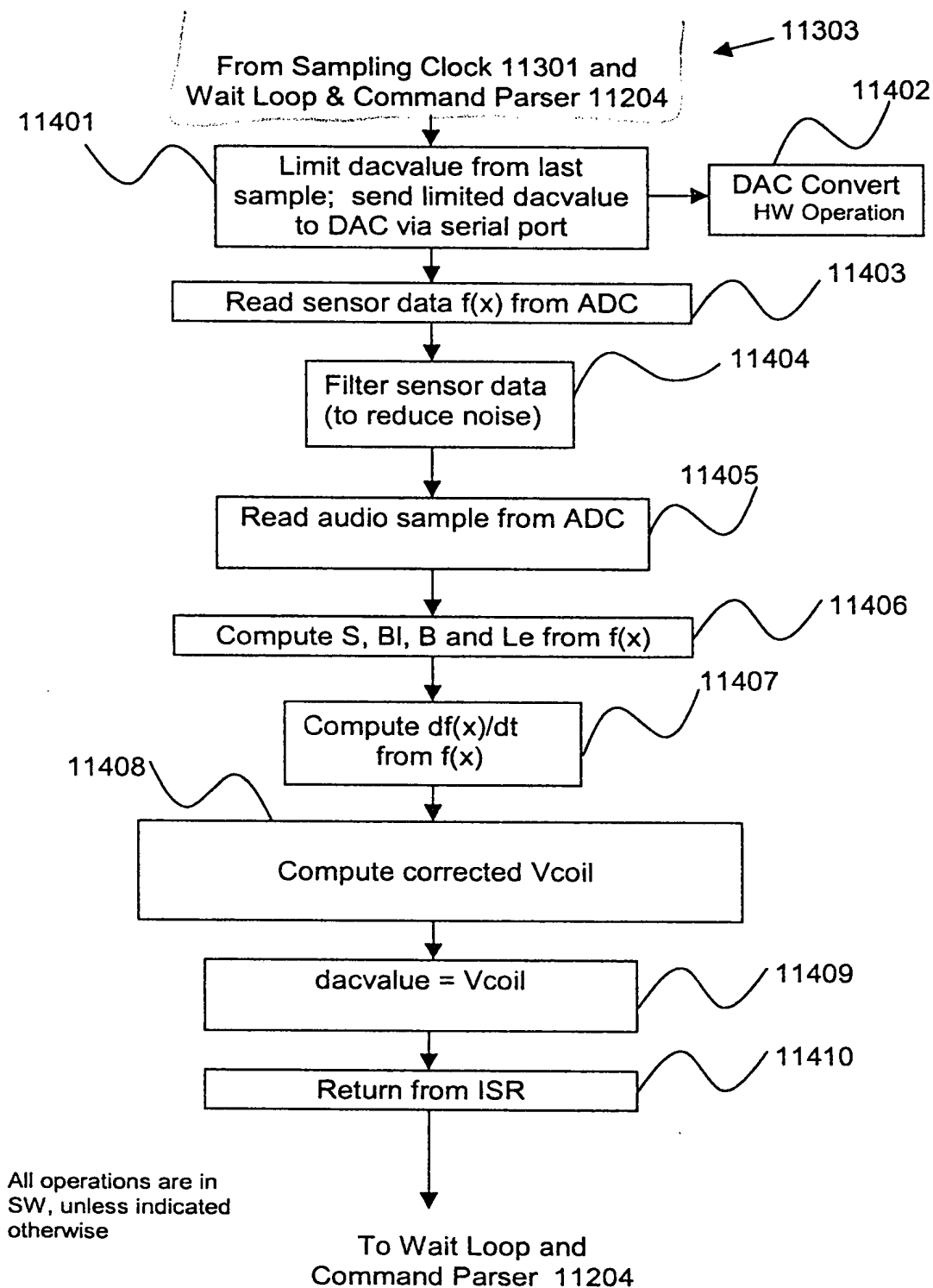


FIG. 48

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From Enable ISR Setup and
 Enable Sampling Clock (SC)
 11202

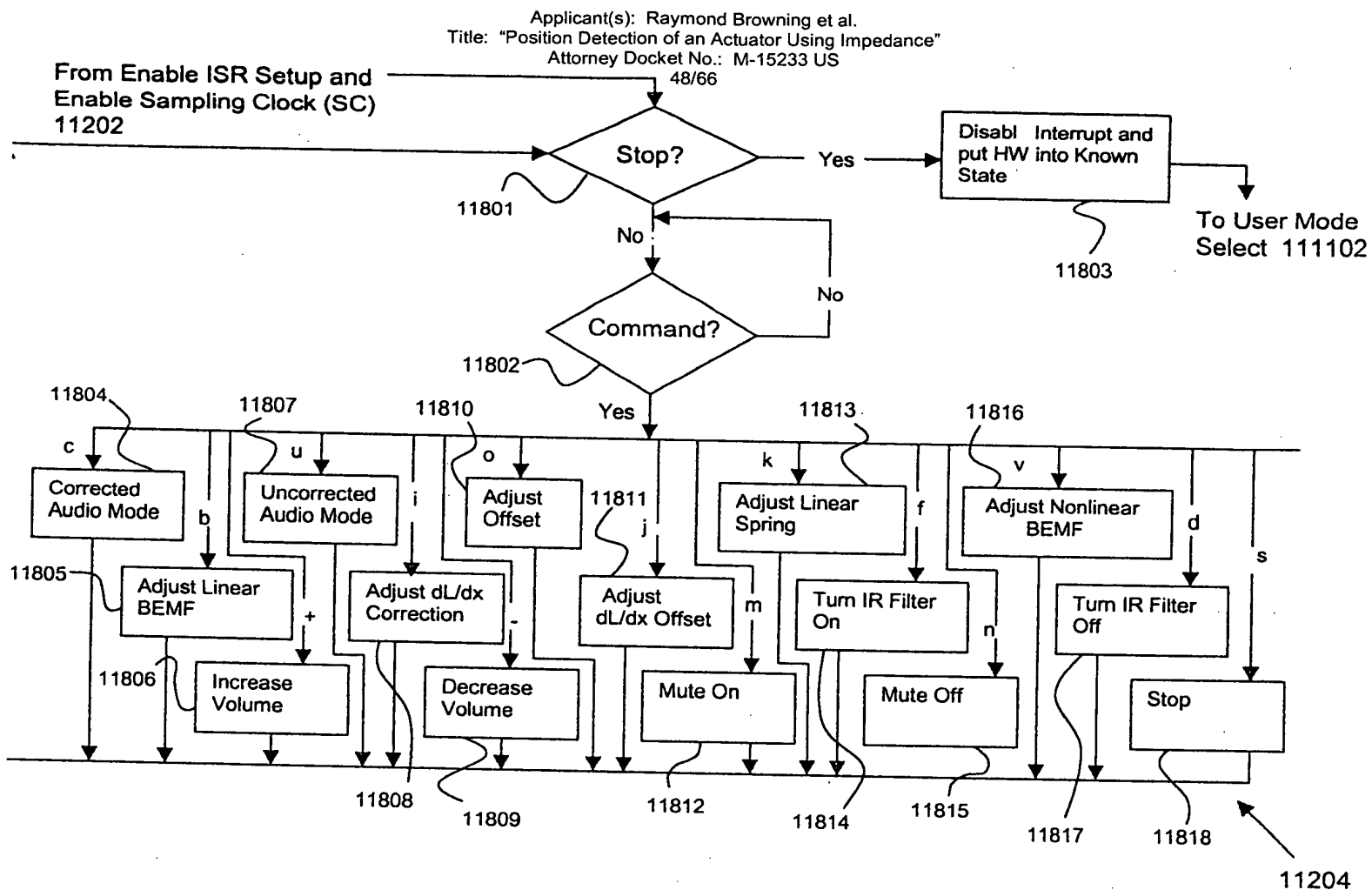


FIG. 49

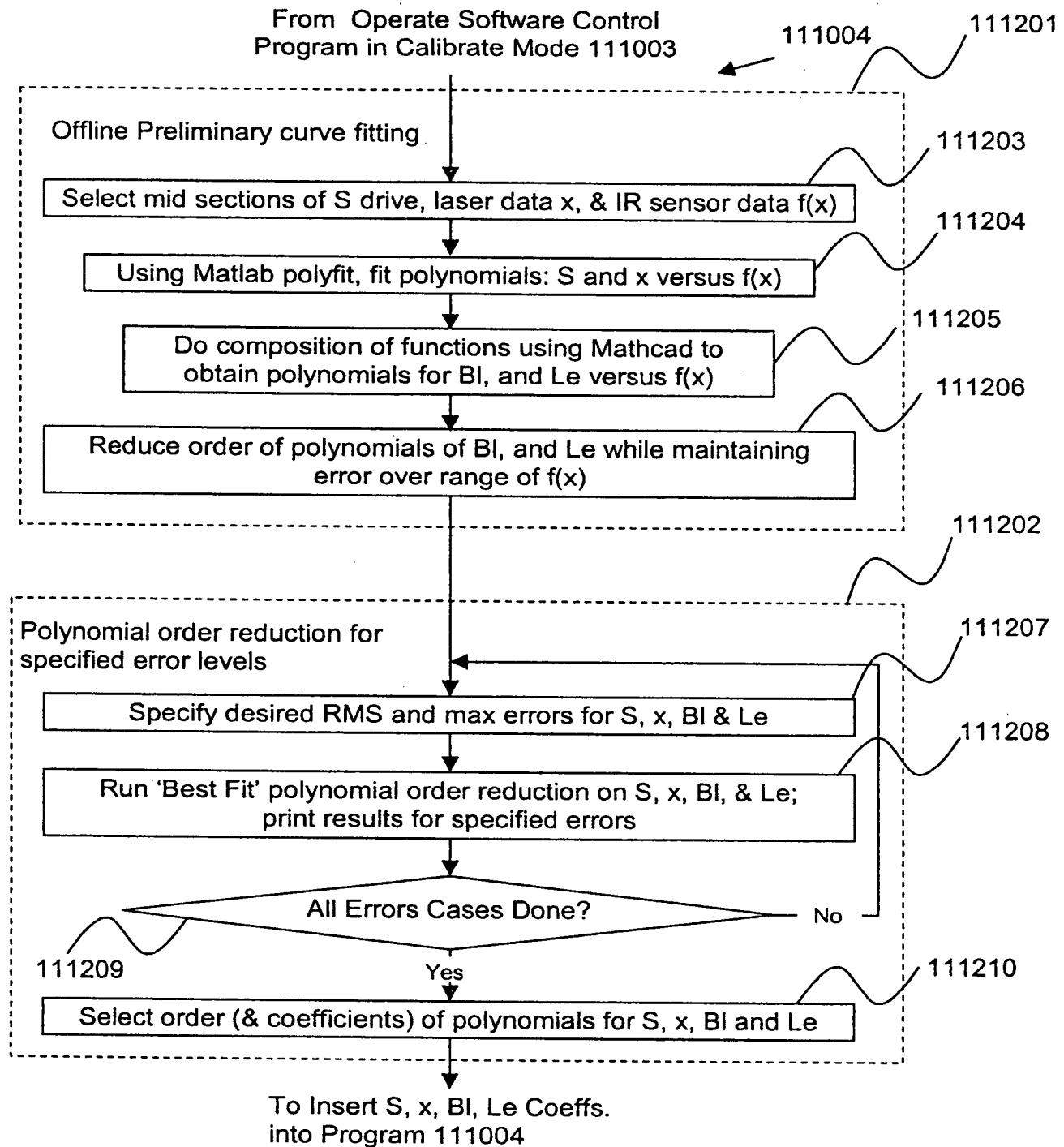


FIG. 50

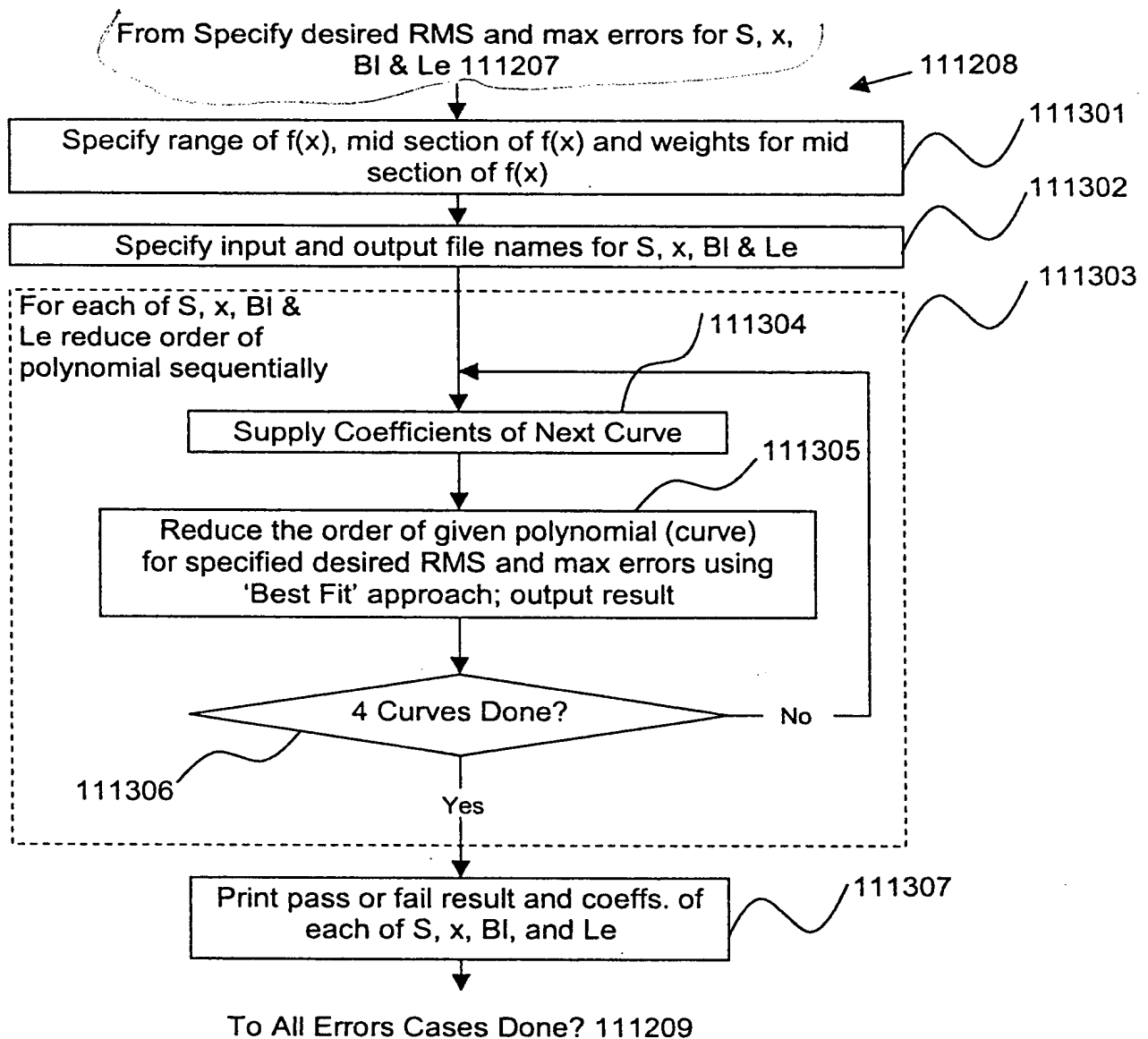


FIG. 51

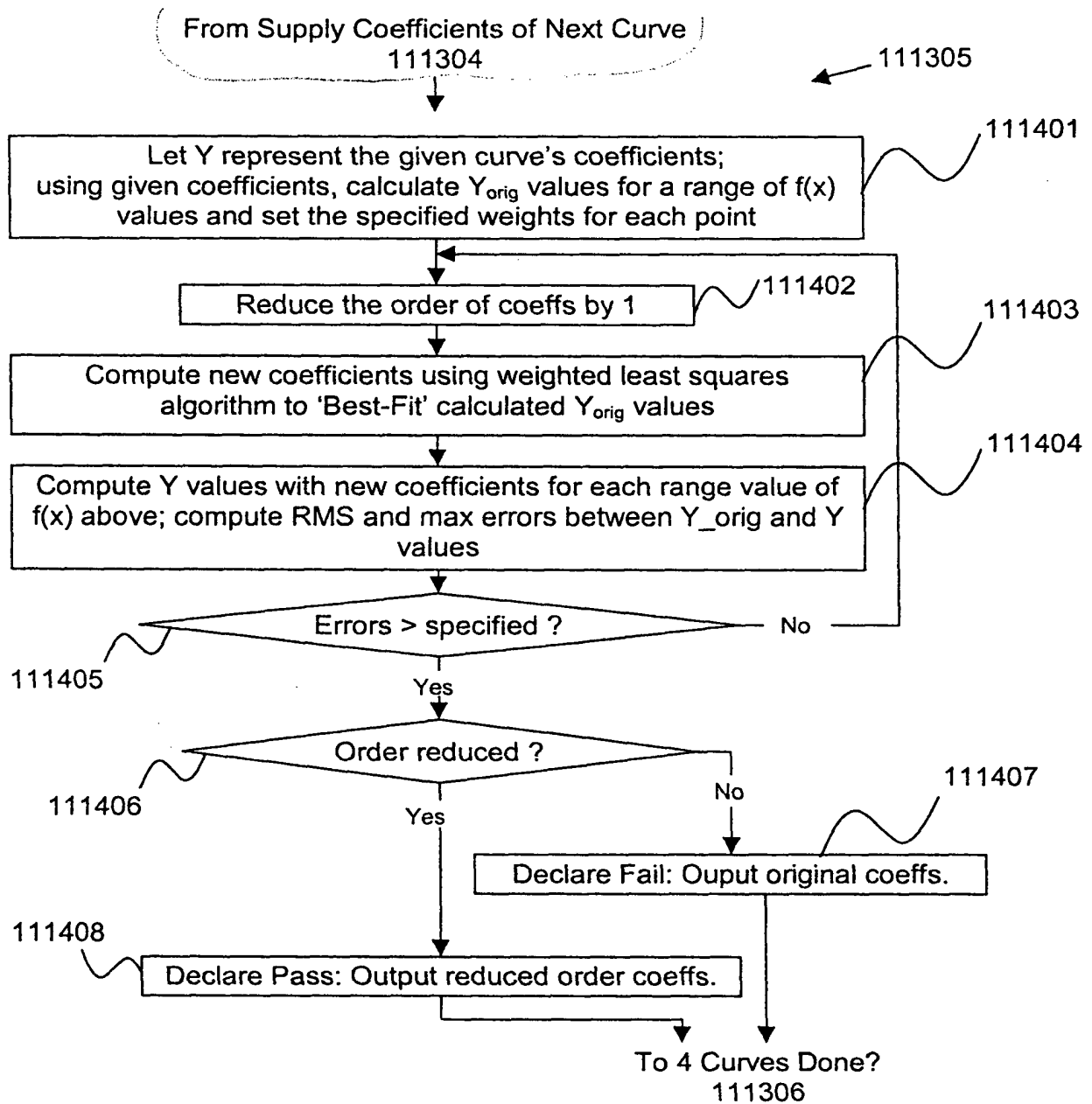


FIG. 52

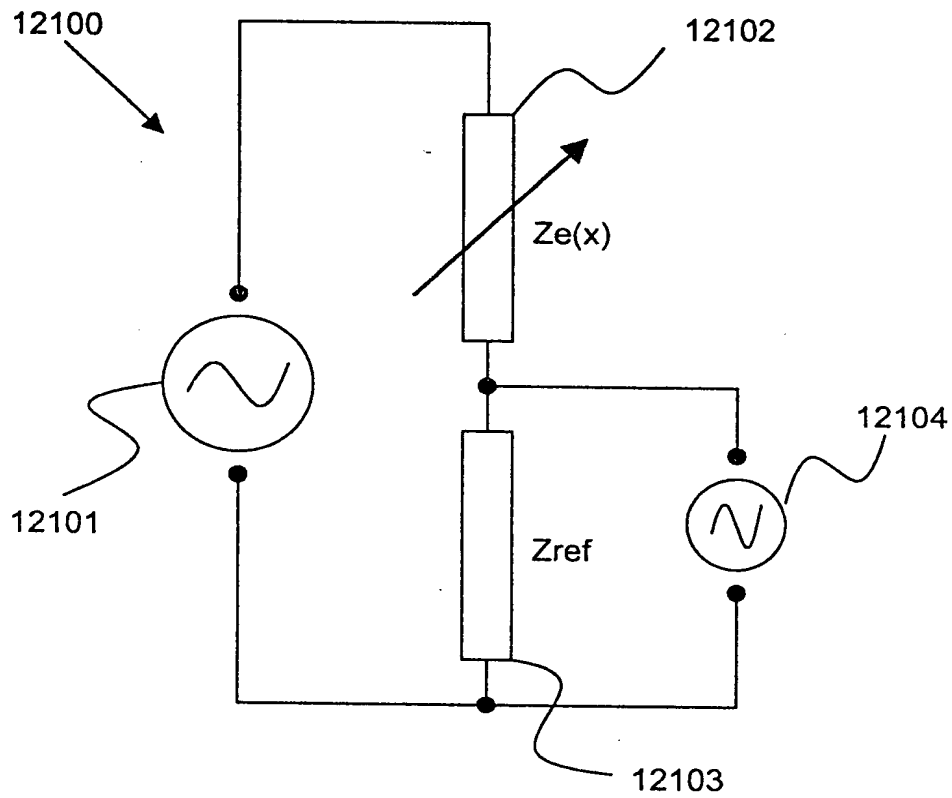


FIG. 53

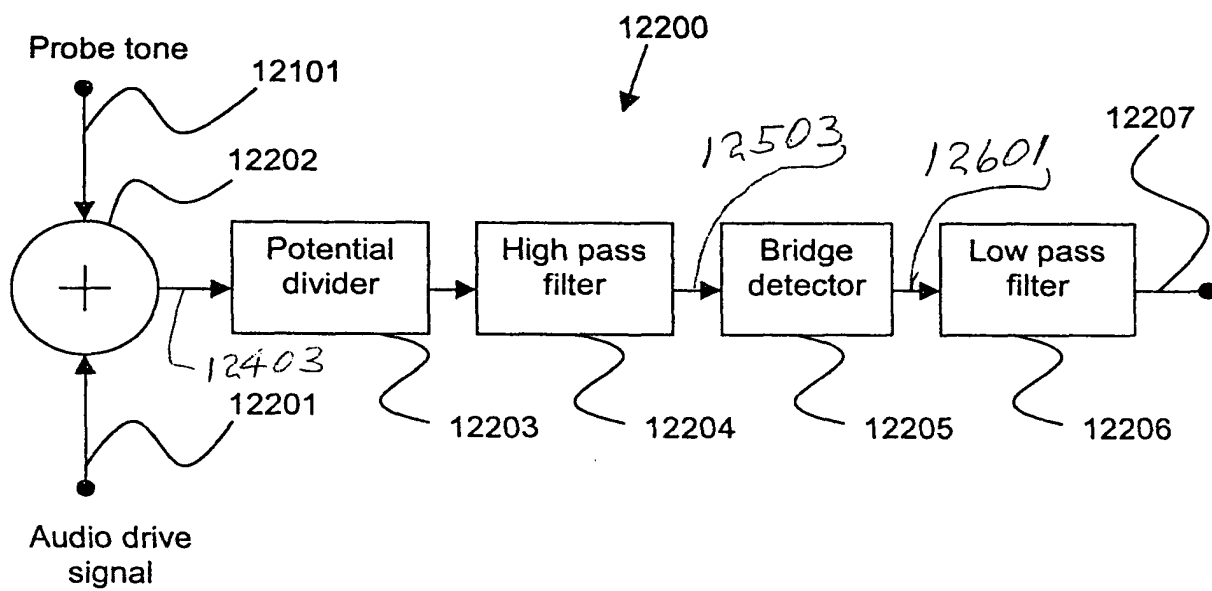


FIG. 54

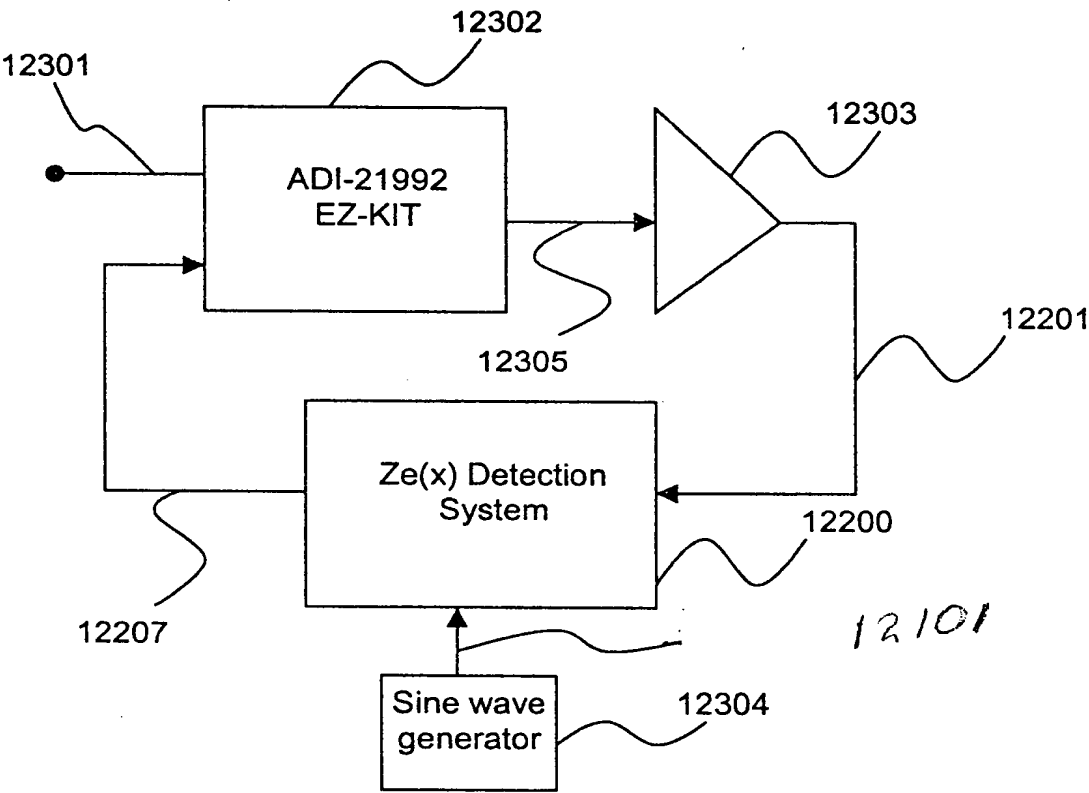


FIG. 55

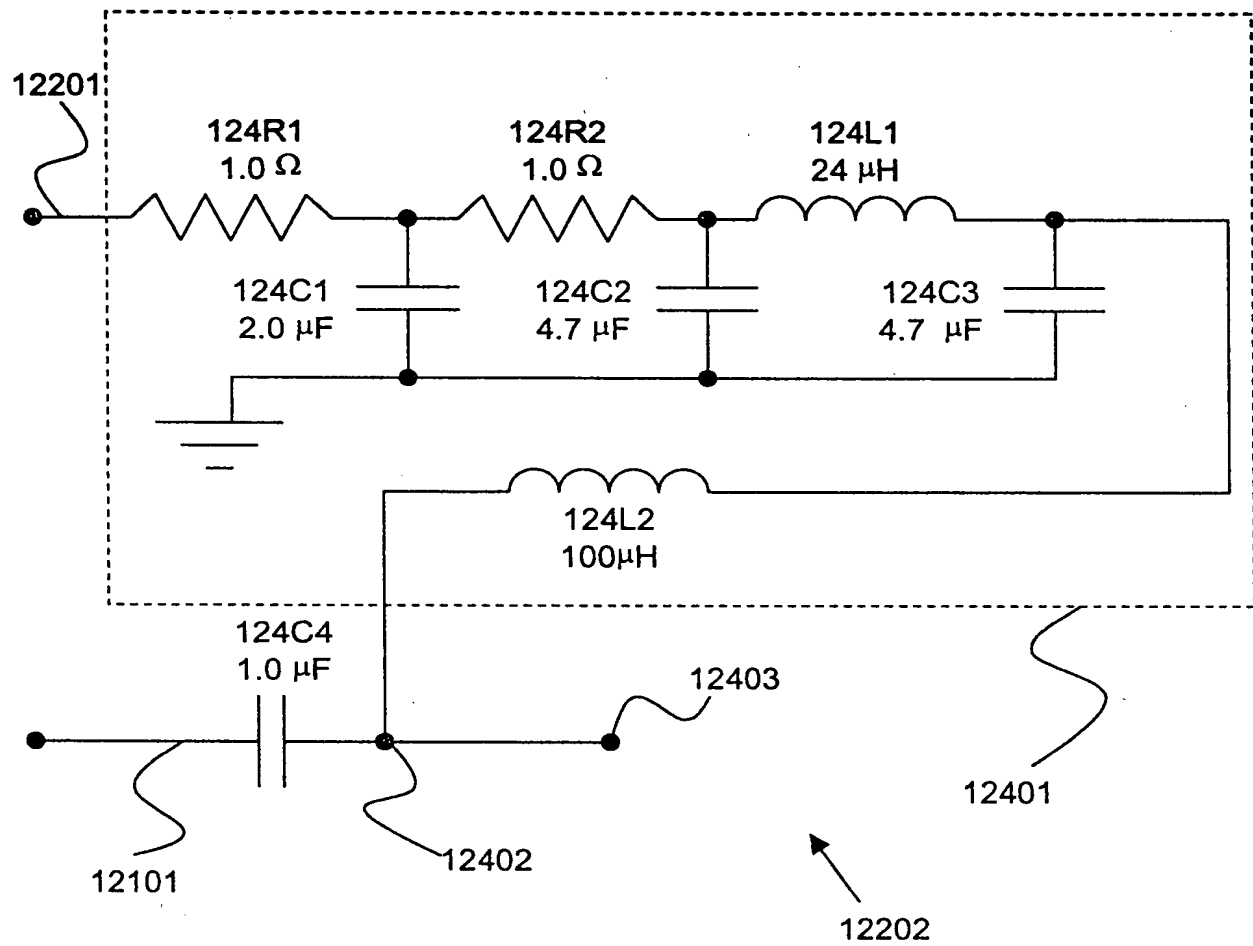


FIG. 56

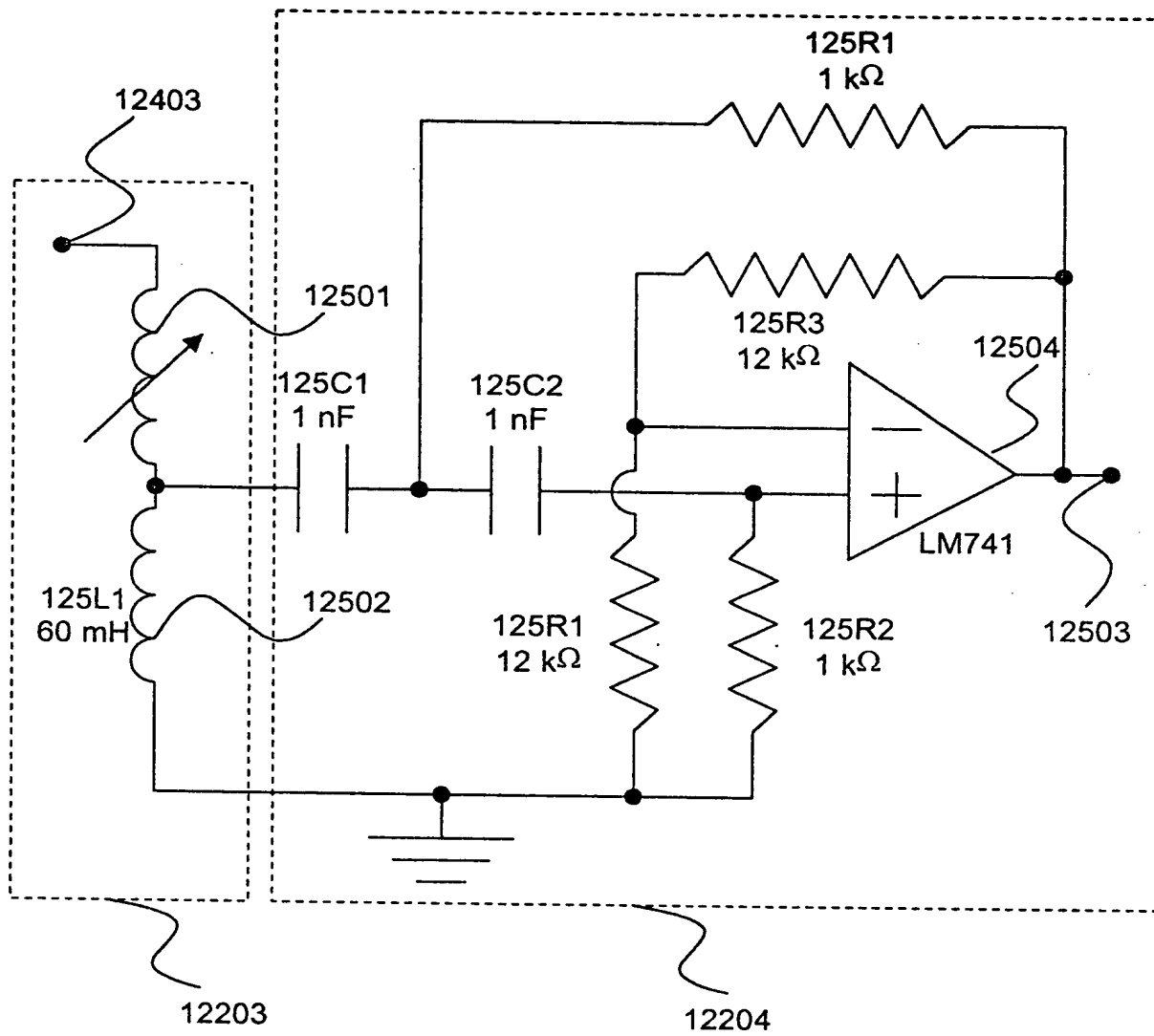


FIG. 57

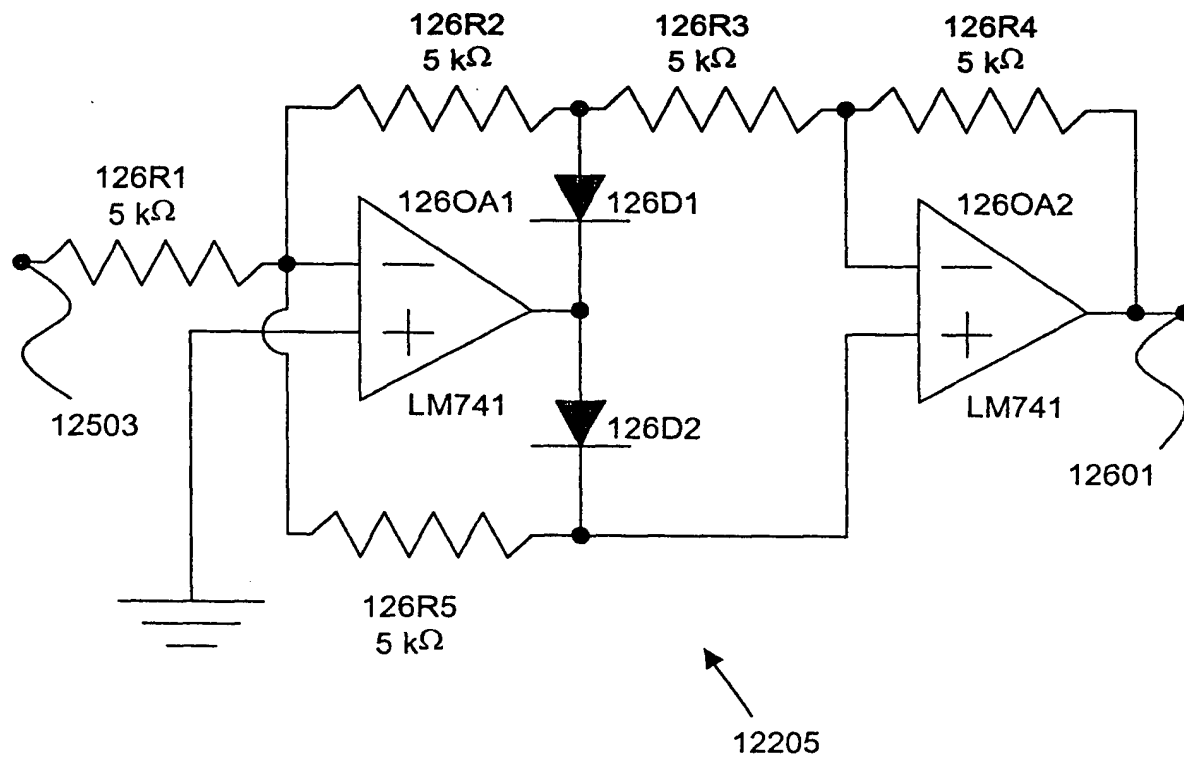


FIG. 58

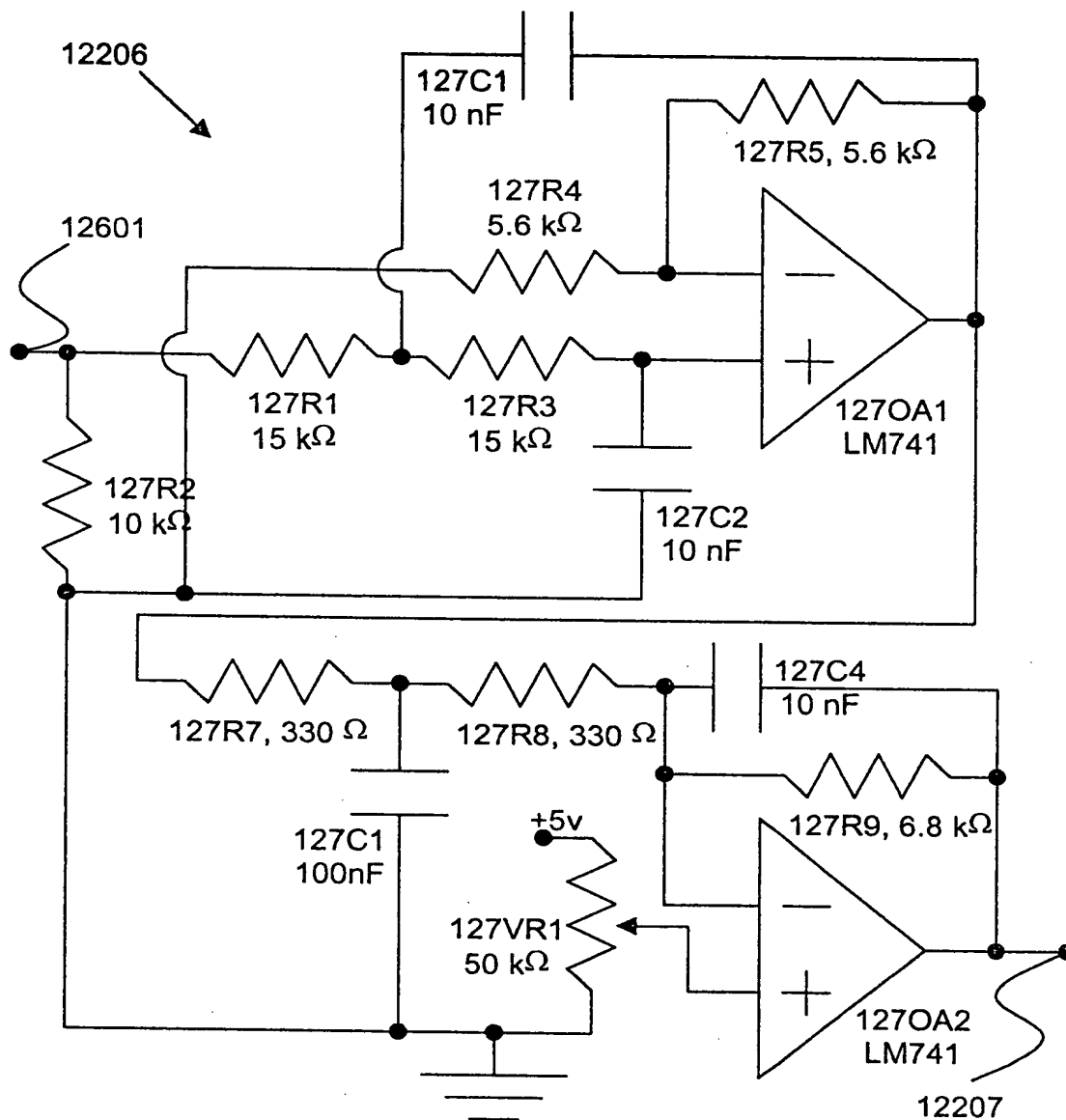


FIG. 59

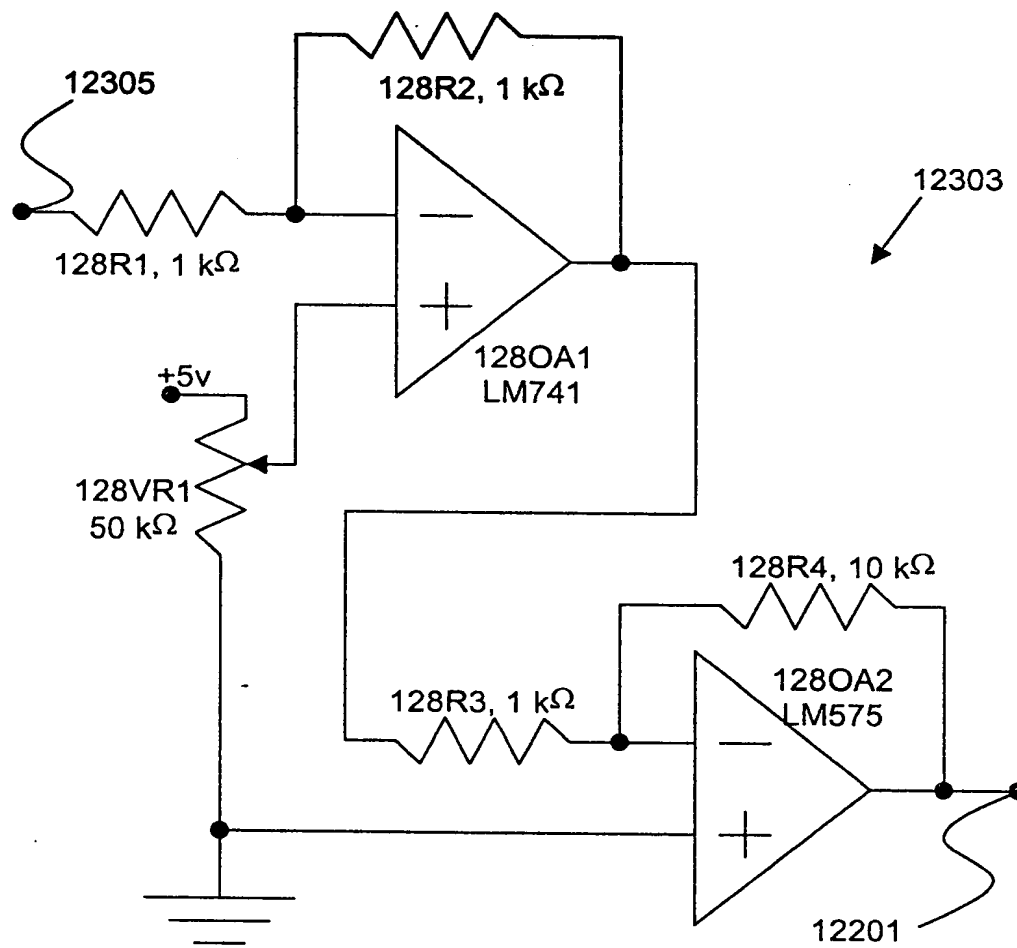


FIG. 60

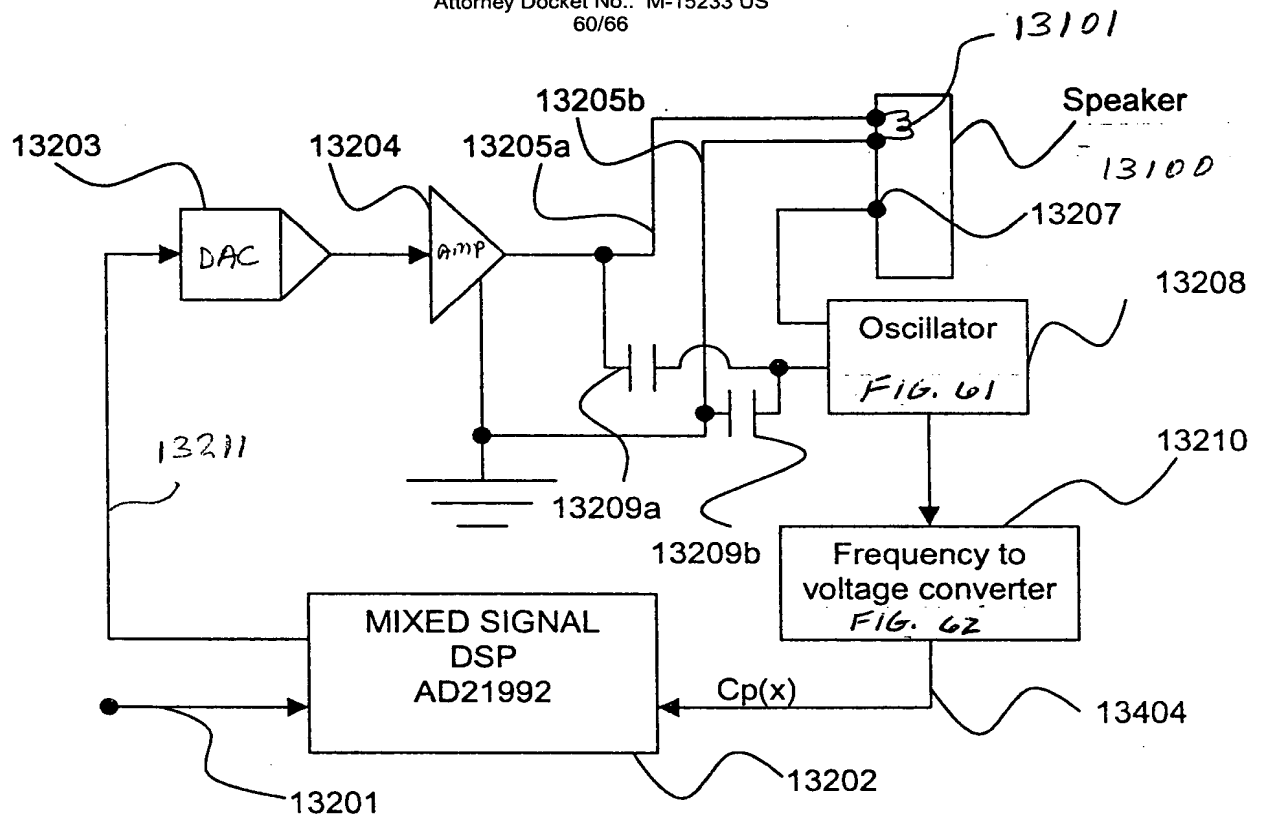


FIG. 61

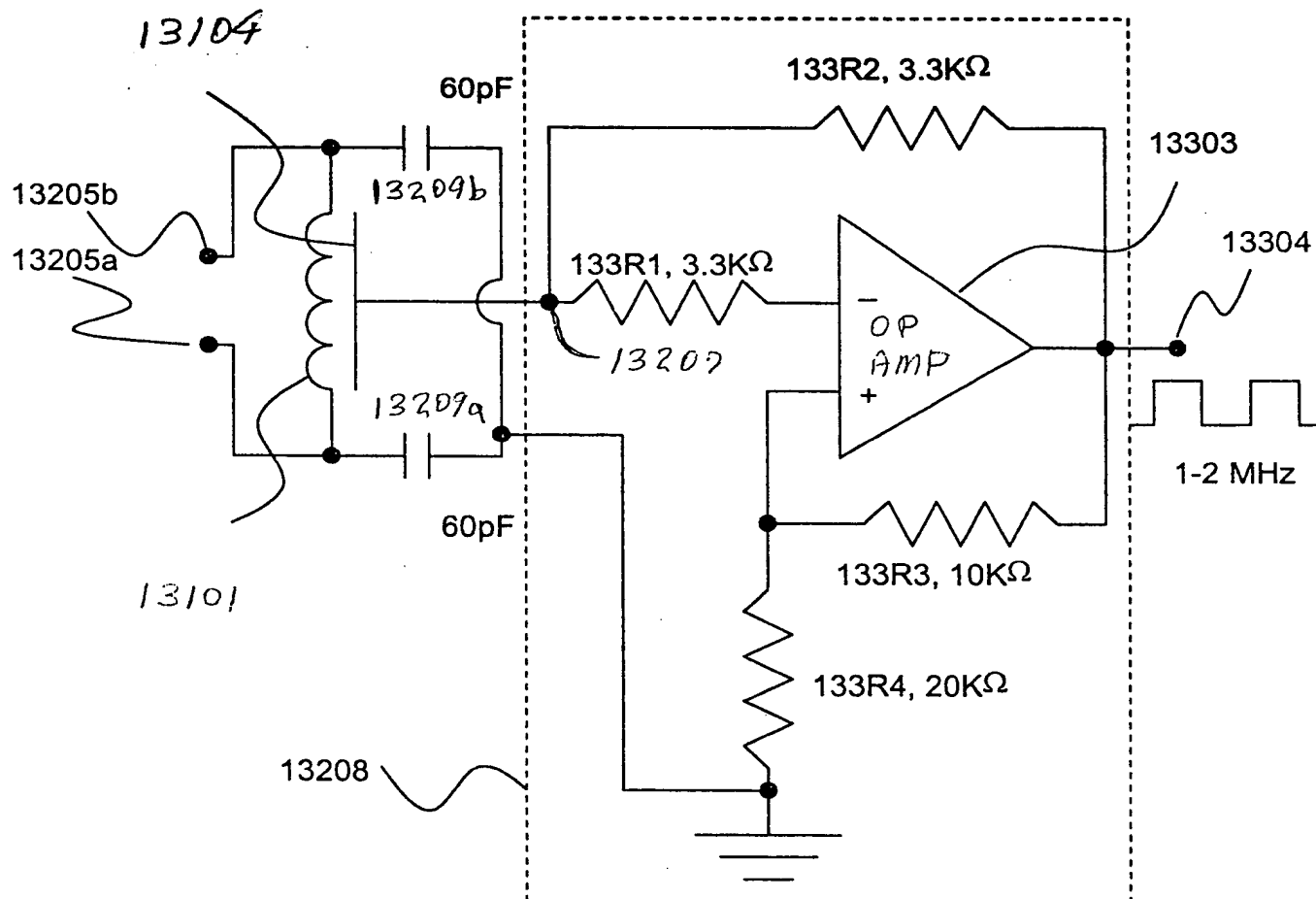


FIG. 62

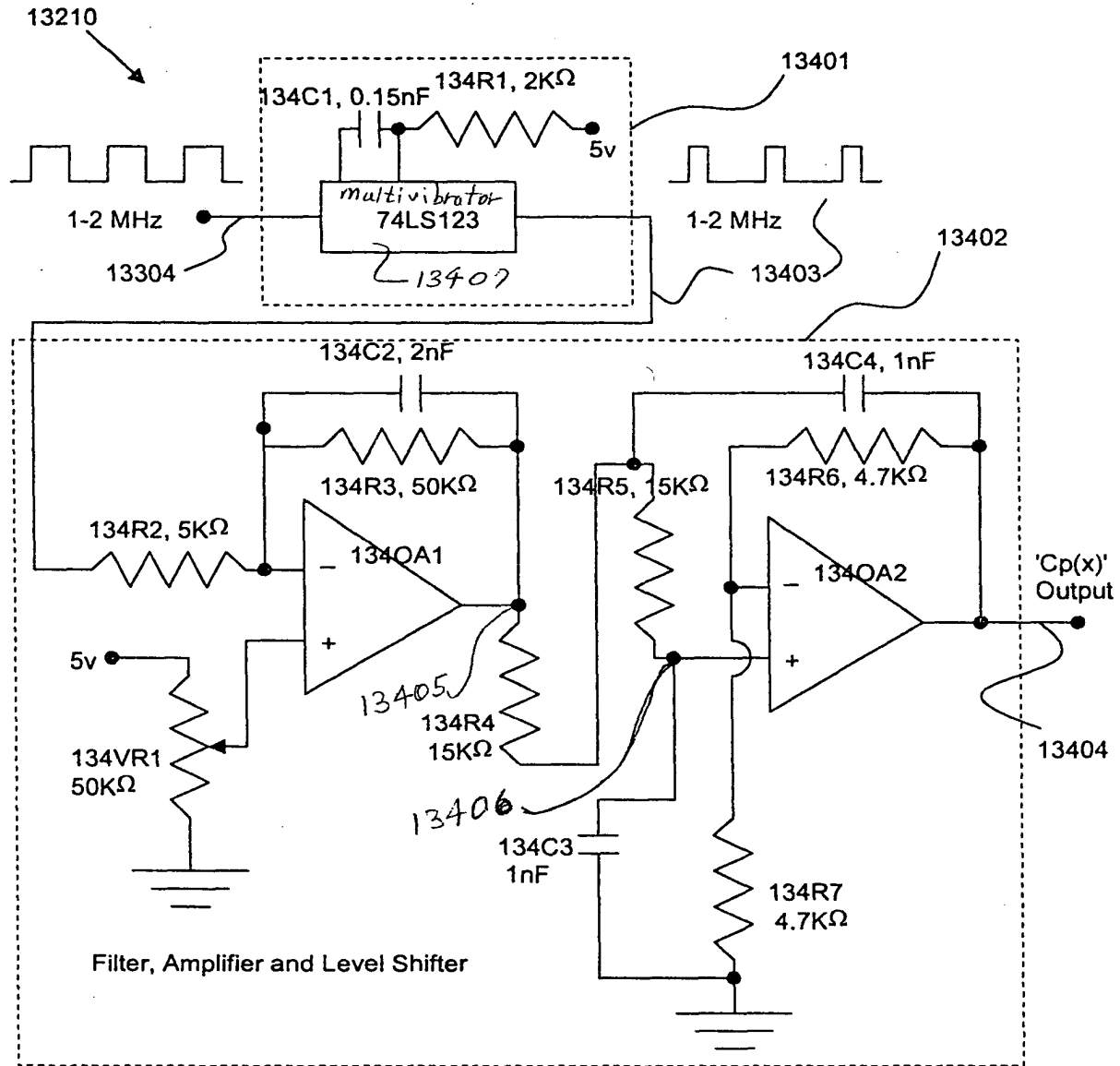


FIG. 63

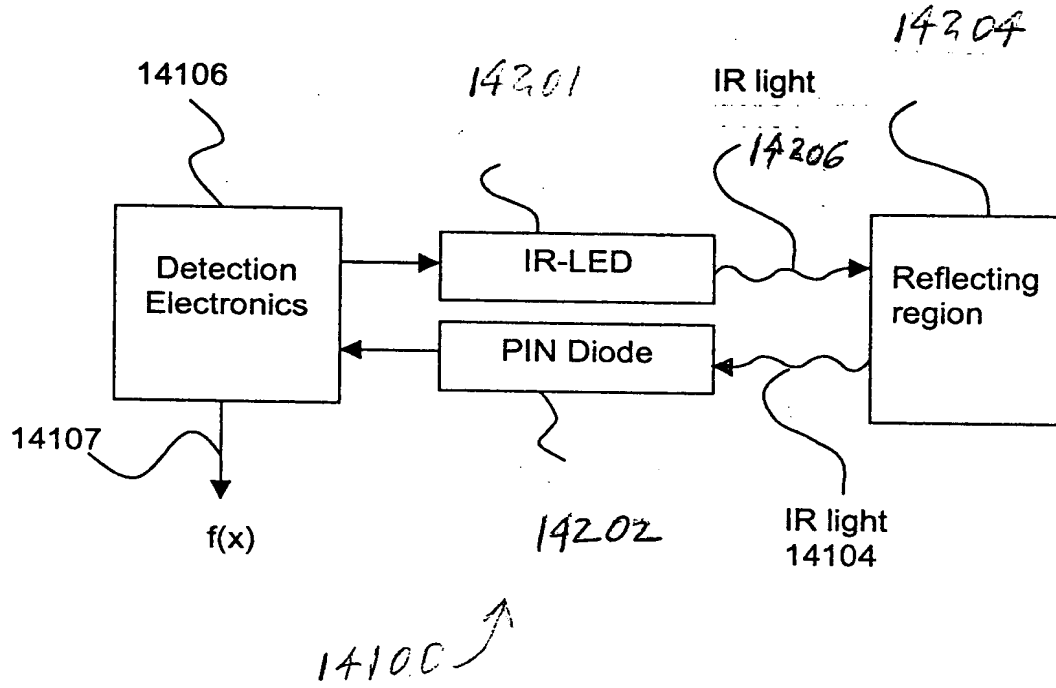
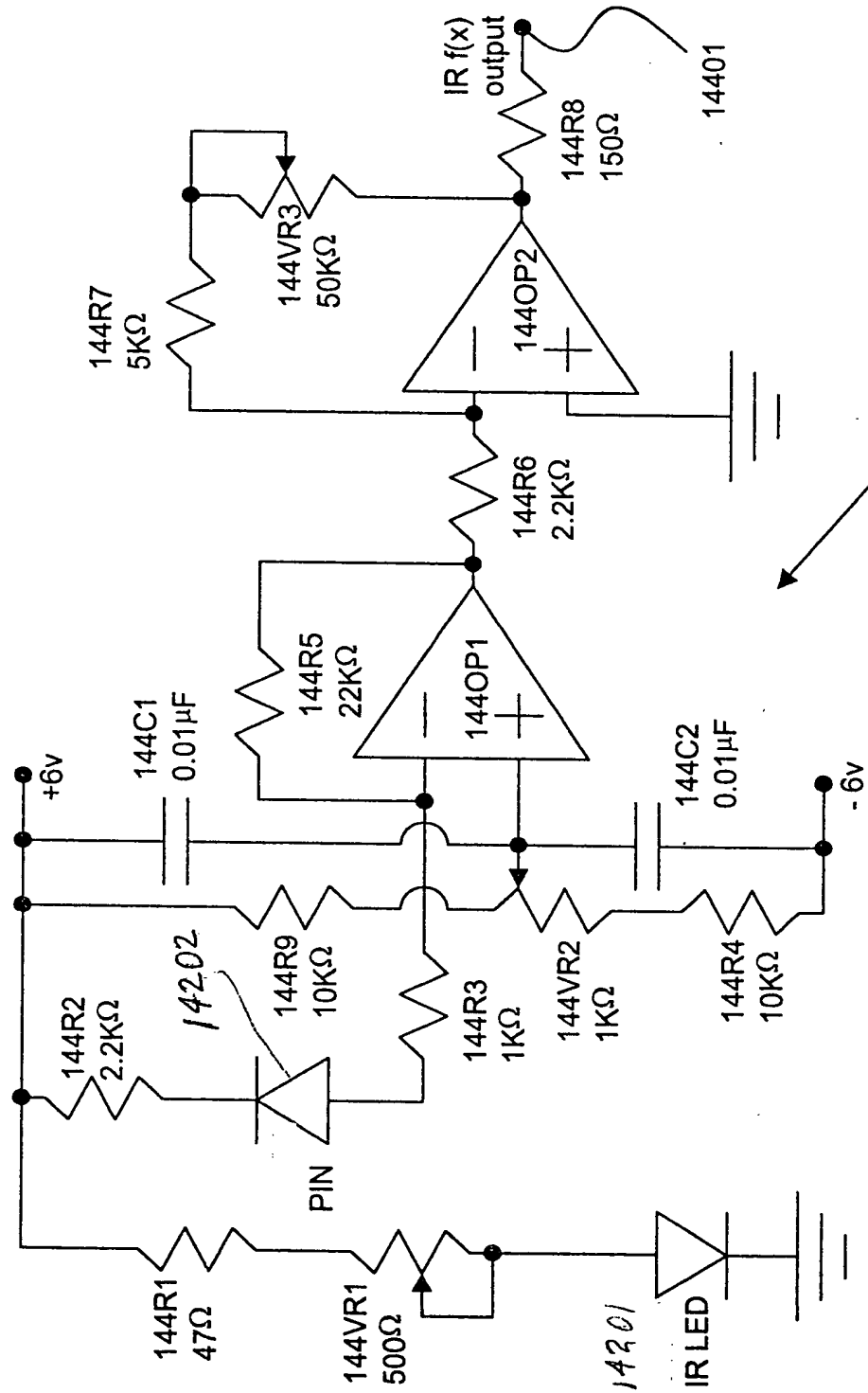


FIG. 64



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FIG. 65

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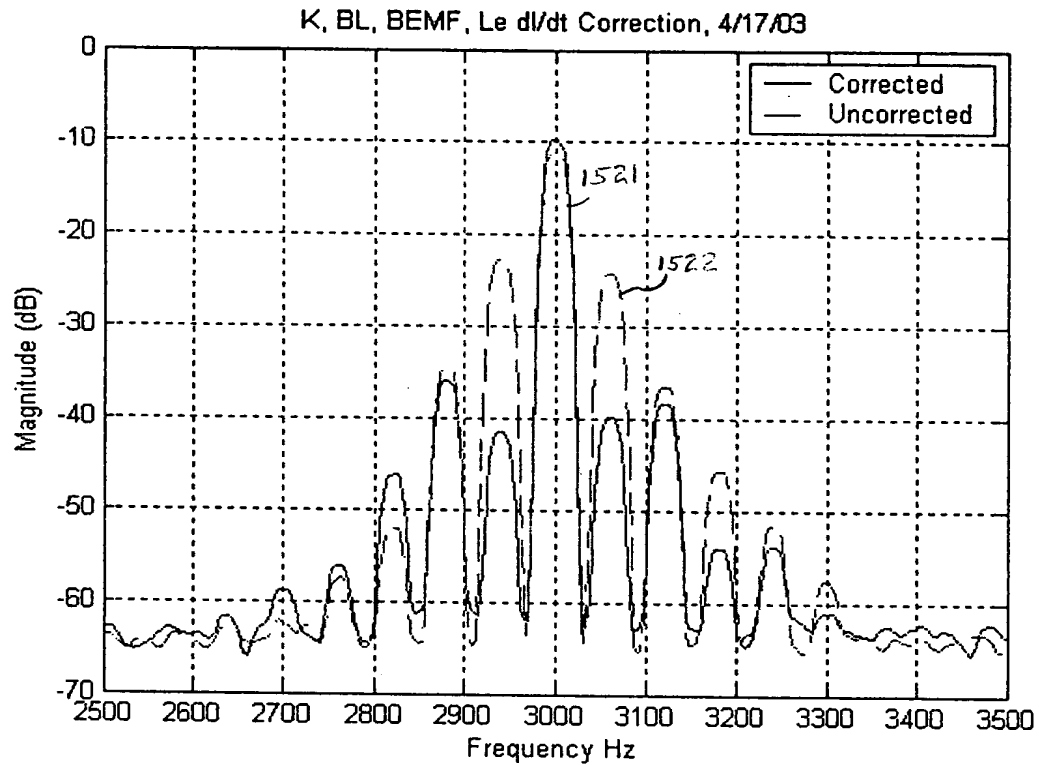


FIG. 66

